

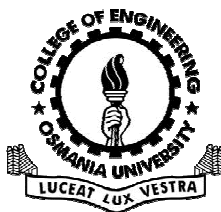


**DEPARTMENT OF  
ELECTRONICS AND COMMUNICATION ENGINEERING**

***Scheme of Instruction  
and  
Syllabus of***

**M.E. (ECE)  
Digital Systems  
Full Time & PTPG**

**2025-26**



**UNIVERSITY COLLEGE OF ENGINEERING  
(Autonomous)  
Osmania University  
Hyderabad – 500 007, TS, INDIA**

**INSTITUTE****Vision**

The Vision of the institute is to generate and disseminate knowledge through harmonious blending of science, engineering and technology. To serve the society by developing a modern technology in student's heightened intellectual, cultural, ethical and humane sensitivities, fostering a scientific temper and promoting professional and technological expertise.

**Mission**

- To achieve excellence in Teaching and Research
- To generate, disseminate and preserve knowledge
- To enable empowerment through knowledge and information
- Advancement of knowledge in Engineering, Science and Technology
- Promote learning in freethinking and innovative environment
- Cultivate skills, attitudes to promote knowledge creation
- Rendering socially relevant technical services to the community
- To impart new skills of technology development
- To inculcate entrepreneurial talents and technology appreciation programmes
- Technology transfer and incubation

**DEPARTMENT****Vision**

- To be in the forefront of advances in Electronics and Communication Engineering education and research to guide and motivate young engineers to face future technological challenges.

**Mission**

- To inculcate analysis and design for innovative problems in the field of Electronics and Communication Engineering with the help of state of art curricula.
- To impart practical training to face real life case studies and inter-disciplinary simple solutions to complex problems.
- To make engineering education an enjoyable learning experience through challenging tutorials, mini-projects, assignments and laboratory exercises.
- To build project team spirit for professional working environment with high ethical values
- To develop overall character that will care for the society and concerned for the nation through extra-curricular activities.

**Programme Educational Objectives (PEO):**

**PEO1:** To educate students with analytical and design skills in Digital systems applicable to Industries, R&D labs and Institutions involving Space Communications and Defense Electronics.

**PEO2:** To strengthen the basic knowledge in mathematical science and applied science with orientation in engineering applications.

**PEO3:** To develop overall personality and character with team spirit, professionalism, integrity, moral and ethical values with the support of humanities, social sciences and physical educational courses.

**PEO4:** To equip the students with laboratory training leading to solving real life practical Problems and project analysis of Digital systems through case-studies, seminars, Miniprojects, internships and main projects.

**Programme Outcomes (PO):**

**PO1:** An ability to independently carry out research /investigation and development work to solve practical problems.

**PO2:** Ability to write and present a substantial technical report/document.

**PO3:** Students should be able to demonstrate a degree of mastery over the area as per the specialization of the program. The mastery should be at a level higher than the requirements in the appropriate bachelor program.

**PO4:** Identify, formulate, Apply and solve engineering problems in the broad areas of Digital System Design using VLSI and Embedded Platforms, Signal Processing and data communication Networks.

**PO5:** Use different software tools in the domains of VLSI and embedded systems for Analysis and Verification.

## DEPARTMENT OF ELECTRONICS &amp; COMMUNICATION ENGINEERING

## M. E. ECE (DIGITAL SYSTEMS) Scheme and Syllabus

| S.No. | Type of Course         | Course Code | Course Name                                   | Contact hours per week |   | Scheme of Evaluation |     | Credits |
|-------|------------------------|-------------|---|------------------------|---|----------------------|-----|---------|
|       |                        |             |   | L                      | P | CIE                  | SEE |         |
|       |                        |             | SEMESTER-I                                    |                        |   |                      |     |         |
| 1.    | Core-I                 | EC101       | Digital Design and PLDs                       | 3                      | - | 40                   | 60  | 3       |
| 2.    | Core-II                | EC102       | Microcontrollers for Embedded Systems Design  | 3                      | - | 40                   | 60  | 3       |
| 3.    | Core-III               | EC103       | VLSI Design Verification and Testing          | 3                      | - | 40                   | 60  | 3       |
| 4.    | Programme Elective-I   | EC111       | VLSI Testing                                  | 3                      | - | 40                   | 60  | 3       |
|       |                        | EC112       | Pattern Recognition                           |                        |   |                      |     |         |
|       |                        | EC113       | IOT and Applications                          |                        |   |                      |     |         |
| 5.    | Programme Elective-II  | EC121       | Advanced Communication and Computer Networks  | 3                      | - | 40                   | 60  | 3       |
|       |                        | EC122       | Wireless and Mobile Communications            |                        |   |                      |     |         |
|       |                        | EC123       | Remote Sensing – Ground Stations              |                        |   |                      |     |         |
| 6.    | Programme Elective-III | EC131       | VLSI Signal Processing                        | 3                      | - | 40                   | 60  | 3       |
|       |                        | EC132       | Optimization Techniques                       |                        |   |                      |     |         |
|       |                        | EC133       | Digital Image and Video Processing            |                        |   |                      |     |         |
| 7.    | Laboratory-I           | EC161       | Embedded Systems Lab                          | 0                      | 2 | 50                   | -   | 1       |
| 8.    | Seminar                | EC171       | Seminar                                       | 0                      | 2 | 50                   | -   | 1       |
|       |                        |             | TOTAL   | 18                     | 4 | 340                  | 360 | 20      |
|       |                        |             | SEMESTER-II                                   |                        |   |                      |     |         |
| 1.    | Core-IV                | EC104       | Hardware Acceleration of Machine Learning     | 3                      | - | 40                   | 60  | 3       |
| 2.    | Core-V                 | EC105       | FPGA Architectures and Applications           | 3                      | - | 40                   | 60  | 3       |
| 3.    | Core-VI                | EC106       | Digital VLSI Design                           | 3                      | - | 40                   | 60  | 3       |
| 4.    | Programme Elective-IV  | EC141       | Design of Fault Tolerant and Testable Systems | 3                      | - | 40                   | 60  | 3       |
|       |                        | EC142       | Real Time Operating Systems                   |                        |   |                      |     |         |
|       |                        | EC143       | DSP Processors and Architectures              |                        |   |                      |     |         |
| 5.    | Programme Elective-V   | EC151       | Scripting Languages                           | 3                      | - | 40                   | 60  | 3       |
|       |                        | EC152       | Unmanned Aerial Vehicle Systems               |                        |   |                      |     |         |
|       |                        | EC153       | Low Power VLSI Design                         |                        |   |                      |     |         |

|                     |                 |         |   |           |           |             |            |           |
|---------------------|-----------------|---------|---|-----------|-----------|-------------|------------|-----------|
| 6.                  | Open Elective   | OE941BM | Medical Assistive Devices                                 | 3         | -         | 40          | 60         | 3         |
|                     |                 | OE942BM | Medical Imaging Techniques                                |           |           |             |            |           |
|                     |                 | OE941CE | Green Building Technology                                 |           |           |             |            |           |
|                     |                 | OE942CE | Cost Management of Engineering Projects                   |           |           |             |            |           |
|                     |                 | OE941CS | Business Analytics  |           |           |             |            |           |
|                     |                 | OE941EC | Elements of Embedded Systems                              |           |           |             |            |           |
|                     |                 | OE941EE | Waste to Energy   |           |           |             |            |           |
|                     |                 | OE942EE | Power Plant Control and Instrumentation                   |           |           |             |            |           |
|                     |                 | OE941ME | Operation Research  |           |           |             |            |           |
|                     |                 | OE942ME | Composite Materials                                       |           |           |             |            |           |
|                     |                 | OE943ME | Industrial Safety   |           |           |             |            |           |
|                     |                 | OE941LA | Intellectual Property Rights                              |           |           |             |            |           |
| 7.                  | Mini Project    | EC172   | Mini Project  | -         | 4         | 50          | -          | 2         |
| 8.                  | Laboratory-II   | EC162   | Digital System Design Lab                                 | -         | 2         | 50          | -          | 1         |
| 9.                  | Laboratory-III  | EC163   | VLSI Design Lab   | -         | 2         | 50          | -          | 1         |
| <b>TOTAL</b>        |                 |         |   | <b>18</b> | <b>8</b>  | <b>390</b>  | <b>360</b> | <b>22</b> |
| <b>SEMESTER-III</b> |                 |         |   |           |           |             |            |           |
| 1.                  | Audit Course-I  | AC030EC | Research Methodology                                      | 2         | -         | 40          | 60         | 0         |
| 2.                  | Audit Course-II | AC031   | English for Research Paper Writing                        | 2         | -         | 40          | 60         | 0         |
|                     |                 | AC032   | Disaster Mitigation and Management                        |           |           |             |            |           |
|                     |                 | AC033   | Sanskrit for technical Knowledge                          |           |           |             |            |           |
|                     |                 | AC034   | Value Education   |           |           |             |            |           |
|                     |                 | AC035   | Stress Management by Yoga                                 |           |           |             |            |           |
|                     |                 | AC036   | Personality Development through Life Enlightenment Skills |           |           |             |            |           |
|                     |                 | AC037   | Constitution of India                                     |           |           |             |            |           |
|                     |                 | AC038   | Pedagogy Studies  |           |           |             |            |           |
|                     |                 | AC039   | E-Waste Management  |           |           |             |            |           |
| 3.                  | Dissertation-I  | EC181   | Dissertation-I  | -         | 20        | 100         |            | 10        |
| <b>TOTAL</b>        |                 |         |   | <b>4</b>  | <b>20</b> | <b>180</b>  | <b>120</b> | <b>10</b> |
| <b>SEMESTER-IV</b>  |                 |         |   |           |           |             |            |           |
| 1.                  | Dissertation-II | EC182   | Dissertation-II   | -         | 32        | 100         | 100        | 16        |
| <b>GRAND TOTAL</b>  |                 |         |   | <b>40</b> | <b>64</b> | <b>1010</b> | <b>940</b> | <b>68</b> |

**Note:** Dissertation-II has two parts, CIE - I and CIE – II, at the end of 8<sup>th</sup> week and 16<sup>th</sup> week respectively for evaluation of 50 marks each.

|                |                         |          |     |   |          |   |
|----------------|-------------------------|----------|-----|---|----------|---|
| EC101          | DIGITAL DESIGN AND PLDs |          |     |   |          |   |
| Core-I         |                         |          |     |   |          |   |
| Pre-requisites | Digital System Design   |          | L   | T | P        | C |
|                |                         |          | 3   | - | -        | 3 |
| Evaluation     | SEE                     | 60 Marks | CIE |   | 40 Marks |   |

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| <b>Course Objectives:</b>  |   |
| The course is taught with the objectives of enabling the student to: |   |
| 1  | To simplify the multiple output functions and design of PLDs  |
| 2  | To know the analysis and synthesis of Synchronous and Asynchronous sequential machines                                      |
| 3  | To understand the Algorithmic State Machine chart for digital designs and minimize the state machines using merger diagrams |

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| <b>Course Outcomes:</b>                                     |  |
| On completion of this course, the student will be able to : |  |
| <b>CO-1</b>   | Minimize of multiple output functions and design using PLDs              |
| <b>CO-2</b>   | Analyze and synthesis of Synchronous sequential machines                 |
| <b>CO-3</b>   | Analyze and synthesis of Asynchronous sequential machines                |
| <b>CO-4</b>   | Draw Algorithmic State Machine chart for digital circuits                |
| <b>CO-5</b>   | Simplify the states using state reduction techniques and merger diagrams |

|            | <b>PO1</b> | <b>PO2</b> | <b>PO3</b> | <b>PO4</b> | <b>PO5</b> |
|------------|------------|------------|------------|------------|------------|
| <b>CO1</b> | 3          | 1          | 3          | 2          | 1          |
| <b>CO2</b> | 1          | 1          | 3          | 3          | 2          |
| <b>CO3</b> | 1          | 1          | 3          | 3          | 3          |
| <b>CO4</b> | -          | -          | 2          | 3          | 2          |
| <b>CO5</b> | 2          | 2          | 3          | 2          | 2          |

Correlation rating: Low / Medium / High: 1 / 2 / 3 respectively.

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| <b>UNIT-I</b>   |
| Tabulation and K-Map minimization method to simplify the multi outputs. Algebraic methods for Determining Prime implicants, Essential implicants. Top- down Modular Combination Logic Design, Computer-aided design of Logic circuits. Combinational circuit Design with Programmable logic Devices (PLDs). |

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| <b>UNIT-II</b>   |
| Introduction to sequential circuits, Sequential circuit model & classification state table and state diagram. Memory devices: Latches and Flip-Flops, excitation table, characteristic equations, conversion of flip-flops and state diagram. Sequential circuits: Mealy and Moore |

models.

**UNIT-III**

Analysis and Synthesis of Synchronous sequential circuits. Synchronous Sequential Circuit Models. Sequential Circuit Analysis. One hot finite state machine design method. Finite State controllers. Algorithmic State Machine (ASM) diagram.

**UNIT-IV**

Analysis and Synthesis of Asynchronous sequential circuits: Analysis of Pulse mode and fundamental mode circuits. Synthesis of Pulse mode circuits. Introduction to Races, Cycles and Hazards.

**UNIT – V**

Simplification of Sequential circuits. Redundant states, State reduction in completely and incompletely specified circuits. Compatible and Incompatible states. Merger diagrams, optimal state assignment methods.

**SUGGESTED READING:**

|   |  |
|---|--|
| 1 | CD Victor, P. Nelson, H Troy Nagle, Bill D. Carrol and J David Irwin. “ <i>Digital Logic Circuit Analysis and Design</i> ”, PHI, 1996. |
| 2 | ZviKohavi, “ <i>Switching and Finite Automata Theory</i> ”, TMH, 2001.   |
| 3 | Parag K. Lala, “ <i>Digital System Design using Programmable Logic Devices</i> ”, 2003, BSP.   |

|                |   |          |     |   |          |   |
|----------------|---|----------|-----|---|----------|---|
| EC102          | MICROCONTROLLERS FOR EMBEDDED SYSTEM DESIGN |          |     |   |          |   |
| Core-II        |   |          |     |   |          |   |
| Pre-requisites | Microprocessors and Microcontrollers        |          | L   | T | P        | C |
|                |   |          | 3   | - | -        | 3 |
| Evaluation     | SEE   | 60 Marks | CIE |   | 40 Marks |   |

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| <b>Course Objectives:</b>  |  |
| The course is taught with the objectives of enabling the student to: |  |
| 1  | Detailed overview of important concepts of Embedded system.            |
| 2  | Analyze PIC microcontroller, its features and programming.             |
| 3  | Describe ARM Microcontroller architectural details and instruction set |
| 4  | Understand ARM Memory management.                                      |
| 5  | Learn the techniques to develop an embedded system and casestudies     |

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| <b>Course Outcomes:</b>                                     |  |
| On completion of this course, the student will be able to : |  |
| <b>CO-1</b>   | Define an embedded system with an overview of important concepts and trends in the design process along with the challenges faced in the embedded systems design |
| <b>CO-2</b>   | Understand the architecture of PIC 18 Microcontroller, its features and programming.   |
| <b>CO-3</b>   | Understand ARM Design Philosophy, architectural details, instruction set and ARM Memory Management.  |
| <b>CO-4</b>   | Analyze and compare the utility and effectiveness of various debugging tools and techniques  |
| <b>CO-5</b>   | Understand Embedded Software development Tools and design a real time based embedded system in the area of communication, automotive, etc.                       |

|            | <b>PO1</b> | <b>PO2</b> | <b>PO3</b> | <b>PO4</b> | <b>PO5</b> |
|------------|------------|------------|------------|------------|------------|
| <b>CO1</b> | 3          | 1          | 2          | -          | -          |
| <b>CO2</b> | 3          | 1          |            | 2          | 3          |
| <b>CO3</b> | 3          |            | 3          | -          | -          |
| <b>CO4</b> | 3          | -          | -          | 3          | 3          |
| <b>CO5</b> | -          | -          | -          | 3          | 3          |

Correlation rating: Low / Medium / High: 1 / 2 / 3 respectively.

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| <b>UNIT-I</b>   |
| <b>Introduction to Embedded Systems:</b> Overview of Embedded System Architecture, Challenges & Trends of Embedded Systems, Hardware Architecture, Software Architecture. |



Application areas of Embedded Systems and Categories of Embedded Systems. Embedded System Design and Co-Design issues and Design Cycle Process.

### UNIT-II

**PIC 18:** Family Overview, Architecture, Instruction Set, Addressing modes. Timers, interrupts of PIC 18, Capture/Compare and PWM modes of PIC 18

### UNIT-III

**ARM Architecture:** ARM Design Philosophy, Registers, Program Status Register, Instruction Pipeline, Interrupts and Vector Table, Architecture Revision, ARM Processor Families. Instruction Set: Data Processing Instructions, Addressing Modes, Branch, Load, Store Instructions, PSR Instructions, Conditional Instructions.

### UNIT-IV

**ARM Thumb Instruction Set:** Register Usage, Other Branch Instructions, Data Processing Instruction Single-Register and Multi Register Load-Store Instructions, Stack, Software Interrupt Instructions. Exception and interrupt handling.

**ARM Memory Management:** Cache Architecture, Policies, Flushing and Caches, MMU, Page Tables, Translation Access Permissions, Context Switch.

### UNIT – V

**Embedded Software Development Tools:** Host and Target Machines, Linkers/Locators for Embedded Software, Getting Embedded Software into the Target System. Debugging Techniques.

### SUGGESTED READING:

|   |   |
|---|---|
| 1 | Raj Kamal, <i>Embedded Systems – Architecture, Programming and Design</i> , 2nd Edition, TMH, 2008  |
| 2 | Andrew N. Sloss, Dominic Symes, Chris Wright, <i>ARM Systems Developer's Guides – Designing &amp; Optimizing System Software</i> , Elsevier, 2008 |
| 3 | Mazidi, MCKinlay and Danny Causey, <i>PIC Microcontrollers and Embedded Systems</i> , Pearson Education, 2007                                     |
| 4 | David.E.Simon, <i>An Embedded Software Primer</i> , 1st Edition, Pearson Education, 1999  |
| 5 | Jonathan W. Valvano, <i>Embedded Microcomputer Systems, Real Time Interfacing</i> , Thomas Learning, 1999.  |

|                |                                      |          |     |   |          |   |
|----------------|--------------------------------------|----------|-----|---|----------|---|
| EC103          | VLSI DESIGN VERIFICATION AND TESTING |          |     |   |          |   |
| Core-III       |                                      |          |     |   |          |   |
| Pre-requisites | -                                    |          | L   | T | P        | C |
|                |                                      |          | 3   | - | -        | 3 |
| Evaluation     | SEE                                  | 60 Marks | CIE |   | 40 Marks |   |

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| <b>Course Objectives:</b>  |   |
| The course is taught with the objectives of enabling the student to: |   |
| 1  | To Develop Structural, Dataflow and Behavioral Modeling of Verilog HDL. |
| 2  | To Know Basics of System Verilog  |
| 3  | To Familiarize with Object Oriented Programming                         |
| 4  | To Explore Randomization and Threads in System Verilog                  |
| 5  | To Know Test Coverage in System Verilog                                 |

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| <b>Course Outcomes:</b>                                     |  |
| On completion of this course, the student will be able to : |  |
| <b>CO-1</b>   | To Realize and Verify Combinational and Sequential Circuits in Verilog HDL |
| <b>CO-2</b>   | To Construct User Defined Data Types in System Verilog                     |
| <b>CO-3</b>   | To Create Object Oriented Programming Environment                          |
| <b>CO-4</b>   | To Demonstrate Randomization and Coverage Concepts of System Verilog       |
| <b>CO-5</b>   | To Propose Efficient Testable Digital Systems in System Verilog            |

|            | <b>PO1</b> | <b>PO2</b> | <b>PO3</b> | <b>PO4</b> | <b>PO5</b> |
|------------|------------|------------|------------|------------|------------|
| <b>CO1</b> | 1          | 2          | 3          | 3          | -          |
| <b>CO2</b> | 1          | 1          | 1          | 1          | -          |
| <b>CO3</b> | 1          | 1          | 3          | 3          | -          |
| <b>CO4</b> | 1          | 1          | 3          | 3          | -          |
| <b>CO5</b> | 2          | 2          | 3          | 3          | 1          |

Correlation rating: Low / Medium / High: 1 / 2 / 3 respectively.

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| <b>UNIT-I Introduction to Verilog</b>  |
| <b>Verilog Basics:</b> Modules and Ports, Data Flow, Behavioral and structural Modeling, Tasks and Functions, Logic Synthesis, Timing Delays.                    |
| <b>Static timing analysis:</b> Setup time & hold time violations and clock skew, Test bench creation, Significance of Verification and Verilog for verification. |

**UNIT–II Introduction to System Verilog****System Verilog Basics:** Advantages over Verilog, Verification process.**Data Types:** Built-in data types, Fixed and dynamic Arrays, Queues, Associative Arrays and Enumerated data types**Statements and Functions:** Procedural statements, Tasks and Functions, Time values, Test bench and DUT**UNIT–III Introduction to Object Oriented Programming (OOP)****OOP:** Object Oriented Programming significance and advantages, classes, objects, object handles, methods, Static and Global Variables, using one class inside another class, Dynamic objects, Copying objects, Public Vs Local and Building a test bench. Inheritance, Overriding, Data Hiding and Encapsulation, Abstract Classes and Virtual Methods. Scope Resolution Operator, Classes Extern Methods, type def classes.**UNIT-IV Verification using System Verilog****Randomization:** Significance of Randomization, randomization in system Verilog, Constraint randomization, Random number generation, constraint tips and techniques.**Threads:** Threads, Inter process communication, Events, Semaphores and Mailboxes virtual methods**UNIT-V Advanced System Verilog**

Callbacks, Parameterized Classes, Static and Singleton Classes

**Coverage:** Introduction to Coverage, Coverage Types, Functional Coverage Strategies and cover group.**Universal Verification Methodology:** Case study using Universal Verification Machine (UVM)**SUGGESTED READING:**

|   |  |
|---|--|
| 1 | Ming-Bo Lin., <i>Digital System Designs and Practices Using Verilog HDL and FPGAs</i> , Wiley India, 2008.   |
| 2 | Samir Palnitkar, <i>Verilog HDL: A Guide to Digital Design and Synthesis</i> , Pearson Education, 2005.      |
| 3 | Christ Spear and Greg Tumbush, <i>System Verilog for Verification</i> , 3 <sup>rd</sup> ed., Springer, 2012. |

|                      |              |          |     |   |          |   |
|----------------------|--------------|----------|-----|---|----------|---|
| EC111                | VLSI TESTING |          |     |   |          |   |
| Program Elective - I |              |          |     |   |          |   |
| Pre-requisites       | -            |          | L   | T | P        | C |
|                      |              |          | 3   | - | -        | 3 |
| Evaluation           | SEE          | 60 Marks | CIE |   | 40 Marks |   |

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| <b>Course Objectives:</b>  |   |
| The course is taught with the objectives of enabling the student to: |   |
| 1  | To model the circuits at different levels of abstraction and to develop logical fault |
| 2  | To understand classical test generation algorithms for combinational and sequential   |
| 3  | To learn scan based testing approaches for digital circuits                           |
| 4  | To understand BIST architectures and ATP generation                                   |
| 5  | To learn diagnosing circuits at logic and system levels                               |

|   |   |
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| <b>Course Outcomes:</b>                                     |   |
| On completion of this course, the student will be able to : |   |
| <b>CO-1</b>   | Develop logic and fault models for digital circuits.        |
| <b>CO-2</b>   | Realize test patterns for the CUT.                          |
| <b>CO-3</b>   | Understand scan based design and DFT concepts.              |
| <b>CO-4</b>   | Understand various generic and specific BIST architectures. |
| <b>CO-5</b>   | Understand fault diagnosis and apply.                       |

|            | <b>PO1</b> | <b>PO2</b> | <b>PO3</b> | <b>PO4</b> | <b>PO5</b> |
|------------|------------|------------|------------|------------|------------|
| <b>CO1</b> | 2          | 2          | 1          | -          | 1          |
| <b>CO2</b> | 2          | 3          | 1          | 1          | 1          |
| <b>CO3</b> | 1          | 1          | 1          | -          | -          |
| <b>CO4</b> | 2          | 1          | 2          | 1          | 1          |
| <b>CO5</b> | 1          | 1          | 1          | 1          | -          |

Correlation rating: Low / Medium / High: 1 / 2 / 3 respectively.

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|--|
| <b>UNIT-I BASICS OF TESTING AND FAULT MODELLING</b>  |
| Introduction to Testing –Functional modeling at various levels of abstraction, Logic Simulation - Types of simulation - Delay models –Element evaluation – Hazard detection - Gate level Event-driven simulation.<br>Faults in digital circuits - Modeling of faults - Logical Fault Models –Faultdetection– Fault equivalent and Fault location - Fault dominance-Fault simulation for Combinational circuits-Fault sampling. |

**UNIT-II TEST GENERATION FOR COMBINATIONAL AND SEQUENTIAL CIRCUITS**

Test generation for combinational logic circuits –one dimensional path sensitization- Boolean difference-PODEM, Testable combinational logic circuit design –Reed-Muller expansion technique- Three level OR-AND-OR design,  
 Test generation for sequential circuits –testing sequential circuits as iterative combinational circuits- State table verification.

**UNIT-III DESIGN FOR TESTABILITY**

Design for Testability –Testability,Ad-hoc designfor testability techniques, Generic scan based design – Full serial integrated scan-Isolated serial scan-non serial scan, Classical scan designs, System level DFT approaches.

**UNIT-IV BIST and ATPG**

Built-In Self Test–BIST concepts, Test pattern generation for BIST- Exhaustive-Pseudo exhaustive –Pseudo random testing, BIST Architectures-Generic OFF line- Specific BIST architectures.

**UNIT – V FAULT DIAGNOSIS**

Logic Level Diagnosis –Fault dictionary, Guided probe testing, Diagnosis by UUT reduction, Fault Diagnosis for Combinational Circuits, System Level Diagnosis-simple model-generalizations of PMC model.

**SUGGESTED READING:**

|   |  |
|---|--|
| 1 | M. Abramovici, M.A. Breuer and A.D. Friedman, "Digital Systems testing and Testable Design", Jaico Publishing House, 2002.                               |
| 2 | Parag. K. Lala, "Fault tolerant and fault testable hardware design",BS Publications, 2002.   |
| 3 | M.L. Bushnell and V.D. Agrawal, "Essentials of Electronic Testing for Digital, Memory and Mixed-Signal VLSI Circuits", Kluwer Academic Publishers, 2002. |

|                      |                     |          |     |   |          |   |
|----------------------|---------------------|----------|-----|---|----------|---|
| EC112                | PATTERN RECOGNITION |          |     |   |          |   |
| Program Elective – I |                     |          |     |   |          |   |
| Pre-requisites       | -                   |          | L   | T | P        | C |
|                      |                     |          | 3   | - | -        | 3 |
| Evaluation           | SEE                 | 60 Marks | CIE |   | 40 Marks |   |

**Course Objectives:**

The course is taught with the objectives of enabling the student to:

|   |  |
|---|--|
| 1 | Understand basic concepts in pattern recognition   |
| 2 | Gain knowledge about state-of-the-art algorithms used in pattern recognition research            |
| 3 | Understand pattern recognition theories, such as Bayes classifier, linear discriminant analysis. |
| 4 | Apply pattern recognition techniques in practical problems.                                      |

**Course Outcomes:**

On completion of this course, the student will be able to :

|             |   |
|-------------|---|
| <b>CO-1</b> | Explain and compare a variety of pattern classification, structural pattern recognition, and pattern classifier combination techniques. |
| <b>CO-2</b> | Understand machine learning concepts and range of problems that can be handled by machine learning.                                     |
| <b>CO-3</b> | Design neural network and SVM for classification.   |
| <b>CO-4</b> | Describe and model data to solve problems in regression and classification  |
| <b>CO-5</b> | Apply pattern recognition techniques to real-world problems such as document analysis and recognition.                                  |

|            | <b>PO1</b> | <b>PO2</b> | <b>PO3</b> | <b>PO4</b> | <b>PO5</b> |
|------------|------------|------------|------------|------------|------------|
| <b>CO1</b> | 1          | 2          | 2          | 2          | 2          |
| <b>CO2</b> | 2          | 3          | 3          | 2          | 2          |
| <b>CO3</b> | 2          | 3          | 3          | 3          | 2          |
| <b>CO4</b> | 3          | 3          | 3          | 3          | 2          |
| <b>CO5</b> | 2          | 2          | 3          | 3          | 1          |

**UNIT-I****Overview of Pattern classification and regression**

What is Pattern recognition; Applications and Examples. Overview of Pattern Classifiers Clustering vs. Classification; Supervised vs. unsupervised. The Bayes Classifier for minimizing Risk Estimating Bayes Error; Minimax and Neymann-Pearson classifiers

**UNIT-II**

Implementing Bayes Classifier; Estimation of Class Conditional Densities Maximum Likelihood estimation of different densities Bayesian estimation of parameters of density functions, MAP estimates Bayesian Estimation examples; the exponential family of densities and ML estimates Sufficient Statistics; Recursive formulation of ML and Bayesian estimates. Mixture Densities and EM Algorithm Mixture Densities, ML estimation and EM algorithm Convergence of EM algorithm; Convergence of EM algorithm; overview of Nonparametric density estimation Nonparametric estimation, Parzen nearest neighbor methods

**UNIT-III****Linear models for classification and regression**

Linear Discriminant Functions; Perceptron --Learning Algorithm and convergence proof Linear Least Squares Regression; LMS algorithm AdaLinE and LMS algorithm; General non liner least-squares regression Logistic Regression; Statistics of least squares method; Regularized Least Squares Fisher Linear Discriminant Linear Discriminant functions for multi-class case; multi-class logistic regression

**UNIT-IV****Artificial Neural Networks for Classification and regression**

Overview of Artificial Neural Networks Multilayer Feed Forward Neural networks with Sigmoidal activation functions; Back propagation Algorithm; Representational abilities of feedforward networks Feedforward networks for Classification and Regression; Back propagation in Practice Radial Basis Function Networks; Gaussian RBF networks Learning Weights in RBF networks; K-means clustering algorithm

**UNIT – V****Support Vector Machines and Kernel based methods**

Support Vector Machines -- Introduction, obtaining the optimal hyperplane SVM formulation with slack variables; nonlinear SVM classifiers Kernel Functions for nonlinear SVMs; Mercer and positive definite Kernels Support Vector Regression and  $\epsilon$ -insensitive Loss function, examples of SVM learning Overview of SMO and other algorithms for SVM;  $\nu$ -SVM and  $\nu$ -SVR; SVM as a risk minimizer Positive Definite Kernels; RKHS; Representer Theorem Bagging and Boosting; Classifier Ensembles; Ada Boost

**SUGGESTED READING:**

|   |  |
|---|--|
| 1 | R. Duda, et al., <i>Pattern Classification</i> , John Wiley & Sons, 2001.        |
| 2 | T. Hastie, et al., <i>The Elements of Statistical Learning</i> , Springer, 2009. |
| 3 | C. Bishop, <i>Pattern Recognition and Machine Learning</i> , Springer, 2006.     |

|                      |                      |          |     |   |          |   |
|----------------------|----------------------|----------|-----|---|----------|---|
| EC113                | IOT and Applications |          |     |   |          |   |
| Program Elective - I |                      |          |     |   |          |   |
| Pre-requisites       | -                    |          | L   | T | P        | C |
|                      |                      |          | 3   | - | -        | 3 |
| Evaluation           | SEE                  | 60 Marks | CIE |   | 40 Marks |   |

**Course Objectives:**

The course is taught with the objectives of enabling the student to:

|   |   |
|---|---|
| 1 | To understand the concepts of Internet of Things and able to build IoT applications |
| 2 | To learn the programming and use of Arduino and Raspberry Pi boards.                |
| 3 | To know about data handling and analytics in SDN.                                   |

**Course Outcomes:**

On completion of this course, the student will be able to :

|             |   |
|-------------|---|
| <b>CO-1</b> | Known basic protocols in sensor networks.                 |
| <b>CO-2</b> | Program and configure Arduino boards for various designs. |
| <b>CO-3</b> | Python programming and interfacing for Raspberry Pi.      |
| <b>CO-4</b> | Design IoT applications in different domains.             |

|            | <b>PO1</b> | <b>PO2</b> | <b>PO3</b> | <b>PO4</b> | <b>PO5</b> |
|------------|------------|------------|------------|------------|------------|
| <b>CO1</b> | 1          | 2          | 2          | 2          | 2          |
| <b>CO2</b> | 2          | 3          | 3          | 2          | 1          |
| <b>CO3</b> | 1          | 3          | 3          | 3          | 2          |
| <b>CO4</b> | 3          | 3          | 3          | 3          | 3          |
| <b>CO5</b> | 2          | 3          | 3          | 3          | 3          |

Correlation rating: Low / Medium / High: 1 / 2 / 3 respectively.

|   |
|---|
| <b>UNIT-I</b>   |
| Introduction to Internet of Things, Characteristics of IoT, Physical design of IoT, Functional blocks of IoT, Sensing, Actuation, Basics of Networking, Communication Protocols, Sensor Networks. |

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|--|
| <b>UNIT-II</b>   |
| Machine-to-Machine Communications, Difference between IoT and M2M, Interoperability in IoT, Introduction to Arduino Programming, Integration of Sensors and Actuators with Arduino |

|  |
|--|
| <b>UNIT-III</b>  |
| Introduction to Python programming, Introduction to Raspberry Pi, Interfacing Raspberry Pi with basic peripherals, Implementation of IoT with Raspberry Pi |



**UNIT-IV**

Implementation of IoT with Raspberry Pi, Introduction to Software defined Network (SDN), SDN for IoT, Data Handling and Analytics.

**UNIT – V**

Cloud Computing, Sensor-Cloud, Smart Cities and Smart Homes, Connected Vehicles, Smart Grid, Industrial IoT, Case Study: Agriculture, Healthcare, Activity Monitoring

**SUGGESTED READING:**

|   |   |
|---|---|
| 1 | "The Internet 'of Things: Enabling Technologies, Platforms, and Use Cases", by PethuruRaj and Anupama C. Raman (CRC Press). |
| 2 | "Make sensors": Terokarvinen, kemo, karvinen and villeyvaltokari, 1st edition, maker media, 2014.                           |
| 3 | "Internet of Things: A Hands-on Approach", by ArshdeepBahga and Vijay Madisetti   |
| 4 | Vijay Madisetti,  |
| 5 | ArshdeepBahga, "Internet of Things: A Hands-On Approach"  |
| 6 | WaltenegusDargie, ChristianPoellabauer, "Fundamentals of Wireless Sensor Networks: Theory and Practice"                     |

|                       |  |          |     |   |          |   |
|-----------------------|--|----------|-----|---|----------|---|
| EC121                 | ADVANCED COMMUNICATION AND COMPUTER NETWORKS |          |     |   |          |   |
| Program Elective - II |  |          |     |   |          |   |
| Pre-requisites        | -  |          | L   | T | P        | C |
|                       |  |          | 3   | - | -        | 3 |
| Evaluation            | SEE  | 60 Marks | CIE |   | 40 Marks |   |

|  |   |
|--|---|
| <b>Course Objectives:</b>  |   |
| The course is taught with the objectives of enabling the student to: |   |
| 1  | Overview of communication computer networks, internet, and foundation of basics   |
| 2  | Study of Routing and Congestion control at the network layer.   |
| 3  | Learn Protocols in Network layer and multicast routing in internetworking and also analyze of protocols Transport layer, and Application Layer. |

|   |  |
|---|--|
| <b>Course Outcomes:</b>                                     |  |
| On completion of this course, the student will be able to : |  |
| <b>CO-1</b>   | Understand advanced concepts in Communication Networking.      |
| <b>CO-2</b>   | Design and develop protocols for Communication Networks.       |
| <b>CO-3</b>   | Understand the mechanisms in Quality of Service in networking. |
| <b>CO-4</b>   | Optimise the Network Design.                                   |
| <b>CO-5</b>   | Analyse protocols multicast routing in internetworking.        |

|            | <b>PO1</b> | <b>PO2</b> | <b>PO3</b> | <b>PO4</b> | <b>PO5</b> |
|------------|------------|------------|------------|------------|------------|
| <b>CO1</b> | 2          | 2          | 2          | 2          | 1          |
| <b>CO2</b> | 3          | 1          | 2          | 2          | 2          |
| <b>CO3</b> | 2          | 1          | 2          | 2          | 2          |
| <b>CO4</b> | 2          | -          | 1          | 1          | 2          |
| <b>CO5</b> | 1          | -          | -          | 1          | 1          |

Correlation rating: Low / Medium / High: 1 / 2 / 3 respectively.

|   |
|---|
| <b>UNIT-I</b>   |
| Data Communications concepts: Data Communications Model Communication Tasks, Networks and Networking configurations and Internet. Foundation of Networking Protocols: 5-layer TCP/IP Model, 7-Layer OSI Model, Internet Protocols and Addressing. |

|   |
|---|
| <b>UNIT-II</b>  |
| Data Link Control protocol: Flow Control, Sliding Window Flow Control, Error control, CRC, ARQ Protocols, Data Link Control, Bit stuffing, HDLC Operation.        |
| Local Area Networks: LAN Architecture. Topologies, Choice of Topology, Ring and Star Usage, MAC and LLC, Generic MAC Frame Format, Multiple Access Protocols, LAN |

|  |
|--|
| Addresses and ARP, Ethernet, Hubs, Bridges and Switches. |
|--|

**UNIT-III**

|  |
|--|
| Switching and multiplexing: Circuit Switching networks and Packet Switching: Packet Switching Principles, Datagram and Virtual Circuit switching, Wide Area Routing: Path Selection Algorithms - Dijkstra's Algorithm, Bellman-Ford Algorithm, Packet Flooding and Deflection Routing Algorithm. |
|--|

**UNIT-IV**

|   |
|---|
| Network layer: Congestion Control at the Network Layer, Internet Protocol: Internetworking, IPv4, IPv6, Transition from IPv4 to IPv6 Multi cast Routing and Protocols: Basic Definitions and Techniques, Internet Group Management Protocol (IGMP). |
|---|

**UNIT – V**

|  |
|--|
| Transport and End-to-End Protocols: User Data gram Protocol (UDP), Transmission Control Protocol (TCP), TCP Congestion Control. With effect from Application Layer: The Web and HTTP, File Transfer: FTP, Electronic Mail in the Internet, Domain Name System (DNS). |
|--|

**SUGGESTED READING:**

|   |  |
|---|--|
| 1 | William Stallings, "Data and Computer Communications", Eighth Edition, Pearson Prentice Hall, 2007.  |
| 2 | Behrouz A. Forouzan, "Data Communications and Networking", Fourth Edition, Tata Mc Graw Hill, 2007.  |
| 3 | Douglas E. Comer, "Internetworking with TCP/IP", Pearson Education, 6th Edition. 4 Prakash and C.gupta "Data communications and computer networks" second Edition, Pearson, PHI learning, 2014 |

|                       |  |          |     |          |   |
|-----------------------|--|----------|-----|----------|---|
| EC122                 | WIRELESS AND MOBILE COMMUNICATIONS               |          |     |          |   |
| Program Elective - II |  |          |     |          |   |
| Pre-requisites        | Analog Communications,<br>Digital Communications | L        | T   | P        | C |
|                       |  | 3        | -   | -        | 3 |
| Evaluation            | SEE  | 60 Marks | CIE | 40 Marks |   |

|  |  |
|--|--|
| <b>Course Objectives :</b> This course aims at |  |
| 1.   | An overview of key wireless technologies: Various generations of mobile communications for voice and data, 5G networks.  |
| 2.   | Wireless system design fundamentals: channel assignment, handoffs, interference, frequency reuse, capacity planning, large-scale fading, and Outdoor, Indoor propagation models. |
| 3.   | Path loss, small-scale fading, multipath, reflection, diffraction, scattering and Various statistical models for small-scale fading study.                                       |
| 4.   | Various Diversity techniques, Equalizers used in communication receivers.  |
| 5.   | Multiple Access techniques and their applications in wireless networks.  |

|   |   |
|---|---|
| <b>Course Outcomes :</b>                                    |   |
| On completion of this course, the student will be able to : |   |
| <b>CO-1</b>   | Develop design models for cellular systems.   |
| <b>CO-2</b>   | Analyze the various Large-scale fading effects in designing propagation models for Mobile communications in Outdoor environments. |
| <b>CO-3</b>   | Analyze the various types of Small-scale fading, measurement techniques, Parameters of multi-path radio and Statistical models.   |
| <b>CO-4</b>   | Understand Various Diversity techniques and Equalizers used in communication receivers.   |
| <b>CO-5</b>   | Develop the design models for various Multiple access techniques and understand their spectral efficiencies.                      |

|             | <b>PO-1</b> | <b>PO-2</b> | <b>PO-3</b> | <b>PO-4</b> | <b>PO-5</b> |
|-------------|-------------|-------------|-------------|-------------|-------------|
| <b>CO-1</b> | 2           | -           | 1           | 2           | -           |
| <b>CO-2</b> | 1           | -           | 1           | 2           | -           |
| <b>CO-3</b> | 1           | -           | 2           | 2           | -           |
| <b>CO-4</b> | 1           | -           | 1           | -           | -           |
| <b>CO-5</b> | 2           | -           | 2           | 2           | -           |

Correlation rating: Low / Medium / High: 1 / 2 / 3 respectively.

|   |
|---|
| <b>Chapter – I</b>  |
| <b>Evolution of Mobile Radio Communications:</b> Examples of Wireless Communication Systems, Overview of 1G,2G, 2.5G, 3G, 4G and 5G Cellular networks.  |
| <b>The Cellular Concept:</b> Introduction, Frequency Reuse, Channel Assignment Strategies, Handoff Strategies, Interference and System Capacity, Improving Coverage and Capacity in cellular systems. |

**Chapter – II**

**Introduction to Radio wave propagation:** Free-space propagation model, Relating Power to Electric Field, The three basic propagation mechanisms- Reflection, Ground Reflection (Two Ray) model, Diffraction, Scattering.

**Outdoor propagation models:** Longley-Rice model, Okumura model, Hata model, PCS Extension to Hata model, Walfisch and Bertoni Model, Wideband PCS Microcell model.

**Indoor propagation models:** Partition losses (same floor), Partition losses between floors, Log-distance path loss model, Ericsson multiple breakpoint model, Attenuation factor model, Signal penetration into buildings.

**Chapter – III**

**Small scale multipath propagation:** Factors influencing small scale fading, Doppler shift, Small scale multipath Measurements-Direct RF Pulse System, Spread Spectrum Sliding correlator Channel Sounding, Frequency Domain Channels Sounding, Parameters of Mobile multipath channels, Types of Small Scale Fading, Statistical models for multipath Fading Channels-Clarke's model for flat fading, spectral shape due to Doppler, Level Crossings and Fading Statistics, Two-ray Rayleigh Fading model..

**Chapter – IV**

**Equalization:** Introduction, Fundamentals of Equalization, Training a Generic Adaptive Equalizer, Equalizers in a communication Receiver, Linear Equalizers, Non linear Equalization-Decision Feedback Equalization (DFE), Maximum Likelihood Sequence Estimation (MLSE) Equalizer, Algorithms for adaptive equalization.

**Diversity Techniques:** Practical Space Diversity Considerations, Selection Diversity, Scanning Diversity, Maximal Ratio Combining, Equal Gain Combining, Polarization Diversity, Frequency Diversity, Time Diversity, RAKE Receiver.

**Chapter –V**

**Multiple Access techniques:** FDMA, TDMA, Spread Spectrum Multiple Access- FHMA and CDMA, SDMA, Spectral efficiency analysis for Multiple Access Technologies: FDMA, TDMA and CDMA Comparison of these technologies based on their signal separation techniques, advantages, disadvantages and application areas.

**Suggested Reading:**

|   |   |
|---|---|
| 1 | Theodore, S.Rappaport , Wireless Communications, Principles and Practice, 2nd Ed.,2002,PHI.                       |
| 2 | Andrea Goldsmith, Wireless Communications, 2005, Cambridge University Press                                       |
| 3 | KavehpahLaven and P.Krishna Murthy, Principles of Wireless networks, 2002,PE.                                     |
| 4 | P.Nicopolitidis, M.S.Obaidat, G.I.Papadimitriou, A.S.Pomportsis,Wireless Networks,2003,John Wiley & Sons Pte Ltd. |
| 5 | Ashok Raj, Wireless Communication, First Edition, 2014, Khanna Publishers   |

|                       |                                  |          |     |   |          |   |
|-----------------------|----------------------------------|----------|-----|---|----------|---|
| EC123                 | REMOTE SENSING – GROUND STATIONS |          |     |   |          |   |
| Program Elective – II |                                  |          |     |   |          |   |
| Pre-requisites        | -                                |          | L   | T | P        | C |
|                       |                                  |          | 3   | - | -        | 3 |
| Evaluation            | SEE                              | 60 Marks | CIE |   | 40 Marks |   |

|  |  |
|--|--|
| <b>Course Objectives :</b>   |  |
| The course is taught with the objectives of enabling the student to: |  |
| 1  | To explain and make the students to understand the basic concepts of Remote sensing platforms and various satellite sensor technologies. |
| 2  | To understand the different types of satellites and various application in the present scenario  |
| 3  | To understand the satellite ground station technology  |
| 4  | To understand the RF systems , servo systems , Base sand systems   |
| 5  | To demonstrate the satellite data quality evaluation and implement the quality assurance aspects   |

|   |  |
|---|--|
| <b>Course Outcomes :</b>                                    |  |
| On completion of this course, the student will be able to : |  |
| <b>CO-1</b>   | Able to understand remote sensing and various applications   |
| <b>CO-2</b>   | Acquires knowledge related to design and development of RF systems , servo systems , Frame synchronizers etc |
| <b>CO-3</b>   | Perform system verification and validation of satellite data quality parameters                              |
| <b>CO-4</b>   | Understands environmental tests and EMI /EMC test standards related to various systems of ground station     |
| <b>CO-5</b>   | Can design and develop electronic hardware for components in the data reception chain                        |

| Course outcome | Program Outcome |      |      |      |      |
|----------------|-----------------|------|------|------|------|
|                | PO-1            | PO-2 | PO-3 | PO-4 | PO-5 |
| <b>CO-1</b>    | 3               | 3    | 3    | 2    | 1    |
| <b>CO-2</b>    | 3               | 3    | 3    | 2    | 2    |
| <b>CO-3</b>    | 3               | 3    | 3    | 2    | 2    |
| <b>CO-4</b>    | 3               | 3    | 3    | 2    | 1    |
| <b>CO-5</b>    | 3               | 3    | 3    | 2    | 2    |

|  |
|--|
| <b>Chapter – I</b>   |
| Physics and principle of Remote Sensing: Electro Magnetic Spectrum, Different remote sensing platforms, Satellite sensors, active sensors and passive sensors, Introduction to space segment , ground segment and user segment,Space technology etc. |

|  |
|--|
| <b>Chapter – II</b>  |
| Satellite orbital dynamics, satellite path and row concepts, reference schemes, Sun synchronous satellites, geostationary satellites, Difference between Geo,MEO and LEO orbits and its requirements, Onboard satellite data conversion and modulation. Signal transmission and EIRP, satellite band |

L,S,X,Ku,Ka . Allocation of bands , Geostationary satellites constellations, IRNSS satellite , GPS constellations, Types and characteristics of different platforms –RESOURCESAT, OCEANSAT, LANDSAT, IRS, INSAT, TERRA, AQUA,NPP etc.

### Chapter – III

Noise survey for ground station, Link margin calculations, Ground station requirements for LEO ,MEO, GEO. Types of antenna mounts, Cassegranian configuration, Prime focus, astronomical mounts, Antenna systems for planetary missions, cross polarisation, isolation, Pointing and tracking accuracy, Auto tracking mechanism, Different tracking mechanisms step track, monopulse track etc, Antenna servo controller, Figure of merit of antenna systems, Front end electronics, Different RF feed systems, RF Downconverters , upconverters, Modulators, Configuration matrix, Demodulators, Frame synchronisers, Data acquisition systems.

### Chapter – IV

Satellite Data receive chain verification and validation, testing of End-end satellite receive chain for high data rate satellites, Servo control systems tests for performance evaluation, Satellite time importance, TCG, Interfacing of ECL,LVDS,TTL signals. Coding mechanisms, phase keying mechanisms and advantages, High speed fiber links for data transfers, RAID controllers and data acquisition systems.

### Chapter – V

Quality assurance of ground station systems, KPI monitoring of RF signal parameters,  $E_b/N_0$ , C/No, IF level. Improving signal to noise, RF performance tuning and signal level monitoring, Servo parameters tuning, tracking gradients, BER verification of satellite data receive chain, IS 9000 environmental test standards for systems/sub systems qualification. CISPR-22 ,IEC 61000 standards for EMI/EMC tests qualification for ground station systems. Overview of ISO 9001:2015 standard.

### Suggested Reading:

|   |   |
|---|---|
| 1 | Bruce Elbert “The satellite communication Ground segment and earth station handbook”, Artech house- 2014, 2 <sup>nd</sup> edition.  |
| 2 | Louis J.Ippolito, Jr “Satellite Communications Systems Engineering” Wiley- 1st Edition, 2008.   |
| 3 | William F. Egan “Practical RF System Design (IEEE Press) Hardcover”, Wiley-IEEE Press, 1 <sup>st</sup> edition 2003   |
| 4 | Warren L.Stutzman, Gary A.thiele “ Antenna theory and design” Wiley , 2012 3 <sup>rd</sup> edition  |
| 5 | Bureau of Indian standards BIS” Basic Environmental testing procedures for electronic and electrical items, Part 32, “ BIS , 2006   |
| 6 | <a href="https://kanchiuniv.ac.in/coursematerials/Notes_Satellite_Commnn_Dr_Umapathy_ECE.pdf">https://kanchiuniv.ac.in/coursematerials/Notes_Satellite_Commnn_Dr_Umapathy_ECE.pdf</a> |
| 7 | Clayton R. Paul,” Introduction to Electromagnetic Compatibility” Wiley-2010, 2nd edition  |

|                        |                        |          |     |   |          |   |
|------------------------|------------------------|----------|-----|---|----------|---|
| EC131                  | VLSI SIGNAL PROCESSING |          |     |   |          |   |
| Program Elective – III |                        |          |     |   |          |   |
| Pre-requisites         | -                      |          | L   | T | P        | C |
|                        |                        |          | 3   | - | -        | 3 |
| Evaluation             | SEE                    | 60 Marks | CIE |   | 40 Marks |   |

|  |   |
|--|---|
| <b>Course Objectives:</b>  |   |
| The course is taught with the objectives of enabling the student to: |   |
| 1  | To enable the students to learn about the concept of pipelining and parallel processing in                            |
| 2  | To make the students to understand the analysis of VLSI system with high speed and low                                |
| 3  | To make the students to understand the concept of Power Reduction and Estimation techniques in VLSI signal processing |

|   |  |
|---|--|
| <b>Course Outcomes:</b>                                     |  |
| On completion of this course, the student will be able to : |  |
| <b>CO-1</b>   | Explain parallel and pipelining processing techniques                        |
| <b>CO-2</b>   | Identify applications for unfolding algorithm                                |
| <b>CO-3</b>   | Analyse Systolic Design for Space Representations containing Delays          |
| <b>CO-4</b>   | Explain Cook-Toom Algorithm, Fast Convolution algorithm by Inspection method |
| <b>CO-5</b>   | Analyze Power Reduction techniques and Power Estimation techniques.          |

|            | <b>PO1</b> | <b>PO2</b> | <b>PO3</b> | <b>PO4</b> | <b>PO5</b> |
|------------|------------|------------|------------|------------|------------|
| <b>CO1</b> | 3          | 3          | 2          | 1          | -          |
| <b>CO2</b> | 3          | 2          | 3          | 2          | -          |
| <b>CO3</b> | 3          | 3          | 1          | 1          | -          |
| <b>CO4</b> | 3          | 3          | 2          | 2          | -          |
| <b>CO5</b> | 2          | 2          | 2          | 2          | 1          |

Correlation rating: Low / Medium / High: 1 / 2 / 3 respectively.

|   |
|---|
| <b>UNIT-I</b>   |
| <i>Introduction to DSP:</i> Typical DSP algorithms, DSP algorithms benefits, Representation of DSP algorithms. <i>Pipelining and Parallel Processing:</i> Introduction, Pipelining of FIR Digital filters, Parallel Processing, Pipelining and Parallel Processing for Low Power, <i>Retiming:</i> Introduction– Definitions and Properties – Solving System of Inequalities – Retiming Techniques. |

|  |
|--|
| <b>UNIT-II</b>   |
| <i>Folding and Unfolding, Folding:</i> Introduction -Folding Transform - Register minimization Techniques – Register minimization in folded architectures – folding of |



multirate systems, Unfolding: Introduction – An Algorithm for Unfolding – Properties of Unfolding – critical Path, Unfolding and Retiming – Applications of Unfolding.

### UNIT-III

*Systolic Architecture Design:* Introduction – Systolic Array Design Methodology – FIR Systolic Arrays – Selection of Scheduling Vector – Matrix Multiplication and 2D Systolic Array Design – Systolic Design for Space Representations contain Delays.

### UNIT-IV

*Fast Convolution:* Introduction – Cook-Toom Algorithm – Winograd algorithm – Iterated Convolution – Cyclic Convolution – Design of Fast Convolution algorithm by Inspection.

### UNIT – V

*Low Power Design:* Scaling Vs Power Consumption–Power Analysis, Power Reduction techniques – Power Estimation Approaches, Programmable DSP: Evaluation of Programmable Digital Signal Processors, DSP Processors for Mobile and Wireless Communications, Processors for Multimedia Signal Processing.

### SUGGESTED READING:

|   |  |
|---|--|
| 1 | Keshab K. Parthi, “VLSI Digital Signal Processing- System Design and Implementation”, 1998, Wiley Inter Science.                               |
| 2 | Kung S. Y, H. J. White House, T. Kailath, “VLSI and Modern Signal processing”, 1985, Prentice Hall.  |
| 3 | Jose E. France, Yannis Tsividis, “Design of Analog – Digital VLSI Circuits for Telecommunications and Signal Processing”, 1994, Prentice Hall. |
| 4 | Mediseti V. K, “VLSI Digital Signal Processing”, IEEE Press (NY), USA, 1995.   |

|                        |                         |          |     |   |          |   |
|------------------------|-------------------------|----------|-----|---|----------|---|
| EC132                  | OPTIMIZATION TECHNIQUES |          |     |   |          |   |
| Program Elective - III |                         |          |     |   |          |   |
| Pre-requisites         | -                       |          | L   | T | P        | C |
|                        |                         |          | 3   | - | -        | 3 |
| Evaluation             | SEE                     | 60 Marks | CIE |   | 40 Marks |   |

|  |  |
|--|--|
| <b>Course Objectives:</b>  |  |
| The course is taught with the objectives of enabling the student to: |  |
| 1  | To introduce various optimization techniques i.e classical, linear                           |
| 2  | Constrained and unconstrained optimization techniques for solving and optimizing             |
| 3  | To explain the concept of Dynamic programming and its applications to project implementation |

|   |  |
|---|--|
| <b>Course Outcomes:</b>                                     |  |
| On completion of this course, the student will be able to : |  |
| <b>CO-1</b>   | Explain the need of optimization of engineering systems  |
| <b>CO-2</b>   | Understand optimization of electrical and electronics engineering problems                             |
| <b>CO-3</b>   | Apply classical optimization techniques, linear programming, simplex algorithm, transportation problem |
| <b>CO-4</b>   | Apply unconstrained optimization and constrained non-linear programming                                |
| <b>CO-5</b>   | Formulate optimization problems.   |

|            | <b>PO1</b> | <b>PO2</b> | <b>PO3</b> | <b>PO4</b> | <b>PO5</b> |
|------------|------------|------------|------------|------------|------------|
| <b>CO1</b> | 2          | 1          | 1          | 1          | -          |
| <b>CO2</b> | 1          | 2          | 1          | 2          | -          |
| <b>CO3</b> | 2          | 2          | 2          | 2          | -          |
| <b>CO4</b> | 2          | 2          | 2          | 1          | -          |
| <b>CO5</b> | 3          | 2          | 2          | 1          | -          |

Correlation rating: Low / Medium / High: 1 / 2 / 3 respectively.

|  |
|--|
| <b>UNIT-I</b>  |
| Use of optimization methods. Introduction to classical optimization techniques, motivation to the simplex method, simplex algorithm, sensitivity analysis. |

|  |
|--|
| <b>UNIT-II</b>   |
| Search methods - Unrestricted search, exhaustive search, Fibonacci method, Golden section method, Direct search method, Random search methods, Univariate method, simplex method, Pattern search method. |

**UNIT-III**

Descent methods, Gradient of function, steepest decent method, conjugate gradient method. Characteristics of constrained problem, Direct methods, The complex method, cutting plane method.

**UNIT-IV**

Review of a global optimization techniques such as Monte Carlo method, Simulated annealing and Tunnelling algorithm.

**UNIT – V**

Generic algorithm - Selection process, Crossover, Mutation, Schema theorem, comparison between binary and floating-point implementation.

**SUGGESTED READING:**

|   |  |
|---|--|
| 1 | SS Rao, " <i>Optimization techniques</i> ", PHI, 1989  |
| 2 | Zhigmiew Michelewicz, " <i>Genetic algorithms + data structures = Evaluation programs</i> ", Springer Verlag - 1992. |
| 3 | Merrium C. W., " <i>Optimization theory and the design of feedback control systems</i> ", McGraw Hill, 1964.         |
| 4 | Weldo D.J., " <i>Optimum seeking method</i> ", PHI, 1964.  |

|                        |                                    |          |     |   |          |   |
|------------------------|------------------------------------|----------|-----|---|----------|---|
| EC133                  | DIGITAL IMAGE AND VIDEO PROCESSING |          |     |   |          |   |
| Program Elective - III |                                    |          |     |   |          |   |
| Pre-requisites         | Digital Signal Processing          |          | L   | T | P        | C |
|                        |                                    |          | 3   | - | -        | 3 |
| Evaluation             | SEE                                | 60 Marks | CIE |   | 40 Marks |   |

|  |   |
|--|---|
| <b>Course Objectives:</b>  |   |
| The course is taught with the objectives of enabling the student to: |   |
| 1  | This course offers fundamentals of digital image and video processing and algorithms  |
| 2  | Beyond the obvious applications in entertainment and scientific visualization, digital  |
| 3  | Through this course, students will get a clear impression of the breadth and practical scope of digital image and video processing and develop a conceptual understanding which will enable them to undertake further study, research, and/or implementation work in this area. |

|   |  |
|---|--|
| <b>Course Outcomes:</b>                                     |  |
| On completion of this course, the student will be able to : |  |
| <b>CO-1</b>   | Define the digital image, representation of the digital image, the importance of image resolution, applications in image processing and also, and application of |
| <b>CO-2</b>   | Select and apply appropriate enhancement and restoration techniques to real problems in image analysis   |
| <b>CO-3</b>   | Understand techniques for image segmentation and techniques for image compression  |
| <b>CO-4</b>   | Develop familiarity with video technology from analog color TV systems to digital  |
| <b>CO-5</b>   | Understand general methodologies for 2D motion estimation, and various coding used in video processing   |

|            | <b>PO1</b> | <b>PO2</b> | <b>PO3</b> | <b>PO4</b> | <b>PO5</b> |
|------------|------------|------------|------------|------------|------------|
| <b>CO1</b> | 2          | 3          | 3          | 2          | 1          |
| <b>CO2</b> | 2          | 3          | 3          | 2          | 1          |
| <b>CO3</b> | 2          | 3          | 3          | 2          | 1          |
| <b>CO4</b> | 1          | 3          | 3          | 2          | 1          |
| <b>CO5</b> | 2          | 3          | 3          | 2          | 1          |

Correlation rating: Low / Medium / High: 1 / 2 / 3 respectively.

|   |
|---|
| <b>UNIT-I</b>   |
| <b>Fundamentals of Image Processing and Image Transforms:</b> Basic steps of Image Processing System, Image acquisition and display, Sampling and Quantization of an image, the basic relationship between pixels image   |
| <b>Image Transforms:</b> 2-D Discrete Fourier Transform, Discrete Cosine Transform (DCT), Haar transform, slant transform, KL transform, singular value decomposition, Radon transform, Wavelet Transforms: Continuous Wavelet Transform, Discrete Wavelet Transforms comparison of different image transforms. |

**UNIT-II****Image Processing Techniques**

**Image Enhancement:** Spatial domain methods: Histogram processing, Fundamentals of Spatial filtering, smoothing spatial filters, Sharpening spatial filters. Frequency domain methods: Basics of filtering in the frequency domain, image smoothing, image sharpening, Selective filtering. Laplacian of Gaussian (LOG) filters.

**Image Restoration:** Introduction to Image restoration, Image degradation, Types of image blur, Classification of image restoration techniques, Image restoration model, Linear and Nonlinear image restoration techniques, Blind deconvolution.

**UNIT-III**

**Image Segmentation:** Segmentation concepts, Point, Line, and Edge Detection, Thresholding, and Region-Based segmentation. Edge detection and linking, Hough Transform, boundary detection, chain coding.

**Image Compression:** Image compression fundamentals - Coding Redundancy, Spatial and Temporal redundancy, Compression models: Lossy & Lossless, Huffman coding, Arithmetic coding, LZW coding, Run-length coding, Bit plane coding, Transform coding, Predictive coding, Wavelet coding, JPEG standards.

**UNIT-IV**

**Basic steps of Video Processing:** Analog Video, Digital Video. Principles of Colour video processing, composite versus component video, Time-Varying Image Formation models Three-Dimensional Motion Models, Geometric Image Formation, Photometric Image Formation, Sampling of Video signals, and Filtering operations.

**UNIT – V**

**2-D Motion Estimation:** Optical flow, General Methodologies, Pixel Based Motion Estimation, Block- Matching Algorithm, Mesh-based Motion Estimation, Global Motion Estimation, Region-based motion Estimation, Multi-resolution motion estimation, Waveform based coding, Block based transform coding, Predictive coding, Application of motion estimation in Video coding, MPEGs and H.26x standards.

**SUGGESTED READING:**

|   |   |
|---|---|
| 1 | Gonzalez and Woods, "Digital Image Processing", 3rd ed., Pearson.                                 |
| 2 | Yao Wang, JoemOstermann and Ya-quin Zhang, "Video processing and communication", 1 stEd., PH Int. |
| 3 | M. Tekalp, "Digital Video Processing", Prentice-Hall International.                               |

| EC161          | EMBEDDED SYSTEMS LAB |   |     |          |   |
|----------------|----------------------|---|-----|----------|---|
| Pre-requisites | -                    |   | L   | T        | P |
|                |                      |   | -   | -        | 2 |
| Evaluation     | SEE                  | - | CIE | 50 Marks |   |

| Course Outcomes:  |  |
|---|--|
| On completion of this course, the student will be able to : |  |
| CO-1  | Use 8051 Microcontroller for various applications                          |
| CO-2  | Use ARM Processor for Interfacing with various Peripherals                 |
| CO-3  | Use Software development tools for 8051 Microcontroller and ARM Processor. |

|     | PO1 | PO2 | PO3 | PO4 | PO5 |
|-----|-----|-----|-----|-----|-----|
| CO1 | 3   | -   | 3   | -   | 3   |
| CO2 | 3   | -   | 3   | -   | 3   |
| CO3 | 3   | -   | 3   | -   | 3   |

1. Interface a simple Switch and Control 8 LEDs with 8051 Microcontroller
2. Write a program to configure Timers of 8051 Microcontroller for generating required delay
3. Display the Hex digits 0 to F on a 7-segment LED interface, with an appropriate delay in between.
4. Write a program to Demonstrate the use of an external interrupt to toggle an LED On/Off
5. Display Hello World message using Internal UART of 8051 Microcontroller.
6. Write a program to configure GPIO pins of ARM controller.
7. Using the Internal PWM module of ARM controller generate PWM and vary its duty cycle.
8. Write a program to Interface 16 x 2 LCD with ARM controller
9. Interface internal ADC of ARM controller to convert the analog to digital and display the same on LCD.
10. Interface a 4x4 keyboard and display the key code on an LCD.
11. Write a program to configure UART of ARM controller
12. Interface a Stepper motor and rotate it in clockwise and anti-clockwise direction using 8 bit or 32 bit Micro controller.

| EC171          | SEMINAR |   |     |          |   |   |
|----------------|---------|---|-----|----------|---|---|
|                |         |   |     |          |   |   |
| Pre-requisites | -       |   | L   | T        | P | C |
|                |         |   | -   | -        | 2 | 1 |
| Evaluation     | SEE     | - | CIE | 25 Marks |   |   |

| Course Objectives :  |  |
|--|--|
| The course is taught with the objectives of enabling the student to: |  |
| 1  | Identify appropriate topic of relevance.   |
| 2  | Update literature on technical articles of selected topic and develop comprehension. |
| 3  | Prepare a technical report.  |
| 4  | Deliver presentation on specified technical topic.                                   |

| Course Outcomes:  |  |
|---|--|
| On completion of this course, the student will be able to : |  |
| CO1   | Develop the habit of referring the journals for literature review. |
| CO2   | Understand the gist of the research paper.                         |
| CO3   | Identify the potential for further scope.                          |
| CO4   | Present the work in an efficient manner.                           |
| CO5   | Write the documentation in the standard format.                    |

Seminar topics may be chosen by the students with advice from the faculty members and the student shall read further relevant articles in the domain.

The seminar must be structured and the PowerPoint presentation shall include following aspects:

1. Introduction to the field
2. Literature survey
3. Consolidation of available information
4. Summary and Conclusions
5. References

Each student is required to:

1. Deliver the seminar for a maximum duration of 30 minutes, where the presentation should be for 20 minutes in PowerPoint, followed by Question and Answers session for 10 minutes.
2. Submit the detailed report of the seminar in spiral bound in a précised format as suggested by the Department.

| Guidelines for awarding marks |                                   |            |
|-------------------------------|-----------------------------------|------------|
| S. No.                        | Description                       | Max. Marks |
| 1                             | Contents and relevance            | 10         |
| 2                             | Presentation skills               | 10         |
| 3                             | Preparation of PPT slides         | 05         |
| 4                             | Questions and answers             | 05         |
| 5                             | The report in a prescribed format | 20         |

**Note:**

1. The seminar presentation should be a gist of at least five research papers from Peer-reviewed journals.
2. **The seminar report should be in the following order:** Background of work, literature review, techniques used, prospective deliverables, discussion on results, conclusions, critical appraisal and reference.
3. At least two faculty members will be associated with the seminar presentation



**SEMESTER-II**

| <b>EC104</b>          | <b>Hardware Acceleration of Machine Learning</b> |          |            |          |          |
|-----------------------|--|----------|------------|----------|----------|
| <b>Pre-requisites</b> | Computer Organization, Micro Processors          | <b>L</b> | <b>T</b>   | <b>P</b> | <b>C</b> |
|                       |  | <b>3</b> | <b>-</b>   | <b>-</b> | <b>3</b> |
| <b>Evaluation</b>     | <b>SEE</b>                                       | 60 Marks | <b>CIE</b> |          | 40 Marks |

**Course Objectives:**

The course is taught with the objectives of enabling the student to:

|   |  |
|---|--|
| 1 | To introduce the fundamentals of parallel computing and explore different parallel architectures |
| 2 | To impart practical knowledge of MPI and CUDA programming  |
| 3 | To develop an understanding of deep learning models  |
| 4 | To analyze and compare accelerator architectures   |
| 5 | To provide hands-on experience with optimization techniques                                      |

**Course Outcomes:**

On completion of this course, the student will be able to :

|             |   |
|-------------|---|
| <b>CO-1</b> | Explain key concepts in parallel computing including speedup, efficiency, scalability, and memory models                            |
| <b>CO-2</b> | Develop parallel programs using MPI for message-passing systems, utilizing point-to-point and collective communication effectively. |
| <b>CO-3</b> | Write CUDA programs that use thread hierarchies and memory management techniques to accelerate image-processing tasks.              |
| <b>CO-4</b> | Compare and evaluate different accelerator architectures (CPU, GPU, TPU, FPGA) for deploying and training deep neural networks.     |
| <b>CO-5</b> | Understand optimization techniques such as pruning, and batching to improve the performance of CNNs on various platforms.           |

|            | <b>PO1</b> | <b>PO2</b> | <b>PO3</b> | <b>PO4</b> | <b>PO5</b> |
|------------|------------|------------|------------|------------|------------|
| <b>CO1</b> | 1          | 1          | 1          | 1          | 1          |
| <b>CO2</b> | 2          | 1          | 1          | 3          | 3          |
| <b>CO3</b> | 2          | 1          | 1          | 3          | 3          |
| <b>CO4</b> | 2          | 1          | 2          | 3          | 3          |
| <b>CO5</b> | 2          | 1          | 1          | 3          | 2          |

Correlation rating: Low / Medium / High: 1 / 2 / 3 respectively.

**UNIT-I**

Introduction to Parallel Computing: Motivation and Concepts, Types of Parallel Architectures: SIMD, MIMD, Shared vs. Distributed Memory, Speedup, Efficiency, Scalability, Introduction to MPI & Cluster Setup (local or cloud-based), MPI Basics:

MPI\_Init, MPI\_Comm\_rank, MPI\_Comm\_size, MPI\_Finalize, Point-to-Point Communication: MPI\_Send, MPI\_Recv, Collective Communication: MPI\_Bcast, MPI\_Reduce, MPI\_Scatter, MPI\_Gather, Synchronization and Barriers, Problem Decomposition: Domain vs. Functional, Load Balancing Techniques, Introduction to Parallel Matrix Multiplication using MPI, Debugging and Profiling MPI Programs

## UNIT-II

**Overview of CUDA Architecture:** CUDA cores and the GPU structure, SIMD (Single Instruction, Multiple Data) concept, **Efficient Memory Management**, Managing device memory, memory allocation strategies, Memory coalescing and data alignment, **Optimizing Performance with Shared Memory**, Shared memory usage for fast data access, Optimizing computational kernels using shared memory, **Reducing Latency**, Asynchronous memory copy, Overlapping computation and communication

**CUDA Programming Basics:** Threads, blocks, and grids, Memory hierarchy (Global, Shared, Local, Constant, Texture memory), CUDA execution model (kernels and thread synchronization), Writing simple CUDA programs, Case Study: CUDA Program for Processing for Image Processing

## UNIT-III

**Overview of Machine and Deep Learning:** Basics of Deep Neural Network (DNN), Training and Inference of DNNs, Forward and Backward propagation Different types, **Networks:** Fully Connected Networks, Convolutional Neural Networks, Matrix Multiplication, Applications in different domains: Computer Vision, Image Classification.

## UNIT-IV

**Accelerator architectures for DNNs:** Introduction to Systolic Array for Matrix-Multiplication, Distinct Characteristics Of Training And Inference, Architectures of TPU v1, v2, v3 and v4; comparison between their architectures, Comparison of CPU, TPU and GPU, Deep Learning techniques on FPGA; efficacy of FPGAs for binarized neural networks (BNNs), Deep Learning on CPU, Deep Learning on GPU

## UNIT – V

**Case Study:** Computer Vision problem example on CPU, GPU, CUDA & FPGA Optimizations techniques: Memory and Compute Optimizations to CNNs such as tiling, loop optimizations, batching, quantization, pruning, Cache Blocking

## SUGGESTED READING:

|   |  |
|---|--|
| 1 | Parallel Programming in C with MPI and Open MP", by Quinn, Michael J.          |
| 2 | NVIDIA- CUDA C Programming Guide – Design Guide                                |
| 3 | Hardware Architectures for Deep Learning: Masoud Daneshtalab, Mehdi Modarressi |

|                |                                     |          |     |   |          |   |
|----------------|-------------------------------------|----------|-----|---|----------|---|
| EC105          | FPGA ARCHITECTURES AND APPLICATIONS |          |     |   |          |   |
| CORE –V        |                                     |          |     |   |          |   |
| Pre-requisites | -                                   |          | L   | T | P        | C |
|                |                                     |          | 3   | - | -        | 3 |
| Evaluation     | SEE                                 | 60 Marks | CIE |   | 40 Marks |   |

**Course Objectives:**

The course is taught with the objectives of enabling the student to:

|   |  |
|---|--|
| 1 | Describe various types of FPGAs /CPLD Architectures.             |
| 2 | Design a digital circuit and implement using FPGA design flow.   |
| 3 | Identify the building blocks of commercially available of FPGAs. |
| 4 | Develop VHDL models and synthesize targeting for FPGAs.          |

**Course Outcomes:**

On completion of this course, the student will be able to :

|             |  |
|-------------|--|
| <b>CO-1</b> | Identify various types of FPGAs Architectures.               |
| <b>CO-2</b> | Design FPGA based system for engineering applications        |
| <b>CO-3</b> | Implement prototype digital systems using design tools.      |
| <b>CO-4</b> | Calculate interconnection delays of Application Specific IC. |
| <b>CO-5</b> | Apply simulators and testing prototype design systems.       |

|            | <b>PO1</b> | <b>PO2</b> | <b>PO3</b> | <b>PO4</b> | <b>PO5</b> |
|------------|------------|------------|------------|------------|------------|
| <b>CO1</b> | 2          | 2          | 2          | 2          | 2          |
| <b>CO2</b> | 2          | 1          | 2          | 2          | 2          |
| <b>CO3</b> | 1          | 3          | 1          | 2          | 2          |
| <b>CO4</b> | 2          | -          | 1          | 2          | 1          |
| <b>CO5</b> | 1          | -          | -          | 2          | 1          |

Correlation rating: Low / Medium / High: 1 / 2 / 3 respectively.

**UNIT-I**

Introduction to FPGAs: Types of ASIC's, Economies of ASIC's, Evolution of Programmable devices, FPGA design flow, Applications of FPGAs.

**UNIT-II**

FPGAs/CPLDs: Class of FPGA's, Commercially available FPGAS, Programming technologies, Commercially available CPLD's , FPGA configuration Logic block for XILINX's Spartan and Vertex FPGA, ACTEL FPGA and Altera CPLD, Programmable I/O blocks, FPGA interconnect Dedicated Specialized components of FPGAs, Technology

|         |
|---------|
| trends. |
|---------|

**UNIT-III**

|  |
|--|
| FPGA physical design: CAD tools, FPGA Partitioning, Partitioning methods. Placement: Goals and objectives, Placement algorithms: Min-cut based placement, Iterative Improvement and simulated annealing. Routing: Global routing: Goals and objectives, Global routing methods, Elmore constant, RC delay and parasitic capacitance, Channel density, Segmented channel routing, Maze routing. |
|--|

**UNIT-IV**

|  |
|--|
| Introduction to VHDL code, data types, behavioral modeling, sequential processing, packages, VHDL synthesis, Verification: Logic simulation, Design validation, Timing verification. |
|--|

**UNIT – V**

|  |
|--|
| Case studies: Designing Examples of VHDL codes for combinational and sequential logic, programmable counter, FIFO, multipliers, ALU, Barrel shifter with FPGA Architectures. |
|--|

**SUGGESTED READING:**

|   |  |
|---|--|
| 1 | Michael John Sebastian Smith, “Application Specific Integrated Circuits”, Pearson Education Asia, 3rd edition 2001       |
| 2 | Pak and Chan, SamihaMourad, “Digital Design using Field Programmable Gate Arrays”, Pearson Education, 1st edition, 2009. |
| 3 | S.Trimberger, Edr, “Field Programmable Gate Array Technology”, Kluwer Academic Publications, 1994.                       |
| 4 | John V. Oldfield, Richard C Dore, “Field Programmable Gate Arrays”, Wiley Publications.                                  |
| 5 | Douglas Perry, “VHDL: Programming by Example”, 2017.   |
| 6 | Volnei A. Pedroni, “ <i>Circuit Design and Simulation with VHDL</i> ” (The MIT Press) second edition.                    |

|                |                     |          |     |   |          |   |
|----------------|---------------------|----------|-----|---|----------|---|
| EC106          | DIGITAL VLSI DESIGN |          |     |   |          |   |
| CORE – VI      |                     |          |     |   |          |   |
| Pre-requisites | -                   |          | L   | T | P        | C |
|                |                     |          | 3   | - | -        | 3 |
| Evaluation     | SEE                 | 60 Marks | CIE |   | 40 Marks |   |

|  |  |
|--|--|
| <b>Course Objectives:</b>  |  |
| The course is taught with the objectives of enabling the student to: |  |
| 1  | To learn the basics of MOS Circuits, its process and understand the operation of MOS   |
| 2  | To impart in-depth knowledge on digital logic design.  |
| 3  | To understand and design the High-speed CMOS logic circuits and acquaint the students with the fundamental concepts of memory and interconnects. |

|   |  |
|---|--|
| <b>Course Outcomes:</b>                                     |  |
| On completion of this course, the student will be able to : |  |
| <b>CO-1</b>   | Able to understand the fabrication process of IC technology      |
| <b>CO-2</b>   | Able to analysis of the operation of MOS transistor              |
| <b>CO-3</b>   | Able to design layout of the logic function                      |
| <b>CO-4</b>   | Student can able to design Adders, Multipliers and memories etc. |
| <b>CO-5</b>   | Getting the idea of design approach                              |

|            | <b>PO1</b> | <b>PO2</b> | <b>PO3</b> | <b>PO4</b> | <b>PO5</b> |
|------------|------------|------------|------------|------------|------------|
| <b>CO1</b> | 3          | 1          | 3          | 3          | 1          |
| <b>CO2</b> | 2          | 1          | 3          | 3          | 3          |
| <b>CO3</b> | 3          | 1          | 2          | 1          | 3          |
| <b>CO4</b> | 2          | -          | 2          | 3          | 3          |
| <b>CO5</b> | 2          | -          | 2          | 2          | 2          |

Correlation rating: Low / Medium / High: 1 / 2 / 3 respectively.

|  |
|--|
| <b>UNIT-I</b>  |
| <i>Review of MOSFET:</i> MOS operation and CMOS process flow, MOS Threshold voltage, Sub threshold conduction. MOSFET I-V characteristics: Long and short channel effects, MOSFET capacitances, lumped and distributed RC model for interconnects, transmission lines, CMOS inverter: Static characteristics, power consumption, dynamic behavior, buffer design. Design rules, Layouts and stick diagram. |

|  |
|--|
| <b>UNIT-II</b>   |
| <i>Combinational logic:</i> Transistor sizing in static CMOS logic gates, static CMOS logic gate sizing considering method of logical effort, dynamic logic, pass-transistor logic and Transmission Gate logic. Sequential logic: Static latches and flip-flops (FFs), dynamic latches and FFs, sense-amplifier based FFs, NORA-CMOS, Schmitt trigger circuit. |

**UNIT-III**

*High Speed CMOS Logic Design:* Switching Time Analysis, Detailed Load capacitance Calculation, Improving Delay calculation with input slope, Gate sizing for optimal path Delay, Optimizing paths with Logical Effort. Scaling MOS Transistors.

**UNIT-IV**

*Data path Design:* Adder, Multiplier, Barrel Shifter, Logarithmic Shifter, Semiconductor Memory Design: Core Memory structure, MOS Decoder, Static RAM cell Design and Content-Addressable Memories (CAM).

**UNIT – V**

*Interconnect Design:* Introduction, Interconnect RC Delays, Buffer Insertion very long wires, Interconnect coupling capacitance: Components of Coupling capacitance, Coupling effects on Delay, Crosstalk, Interconnect Inductance. Timing issues: Timing fundamentals, clock distribution and jitter.

**SUGGESTED READING:**

|   |  |
|---|--|
| 1 | David A Hodges, Horace G Jackson ResveASaleg “ <i>Analysis and Design of Digital Integrated circuits</i> ” The McGraw Hill Companies 3rd edition, 2006 |
| 2 | Jan M Rabaey, A Chandrakasan, Borvioje N “ <i>Digital Integrated Circuits Design Perspective</i> ” PHI 2nd edition, 2005.                              |
| 3 | Neil H E Weste, David Harris, Ayan Banerjee “ <i>CMOS VLSI Design a circuit’s and system perspective</i> ” Pearson 3rd Edition 2009.                   |
| 4 | Wayne Wolf, “ <i>Modern VLSI Design</i> ” 3rd ed., 1997, Pearson Education   |

|                       |   |          |     |   |          |   |
|-----------------------|---|----------|-----|---|----------|---|
| EC141                 | DESIGN OF FAULT TOLERANT AND TESTABLE SYSTEMS |          |     |   |          |   |
| Program Elective - IV |   |          |     |   |          |   |
| Pre-requisites        | -   |          | L   | T | P        | C |
|                       |   |          | 3   | - | -        | 3 |
| Evaluation            | SEE   | 60 Marks | CIE |   | 40 Marks |   |

**Course Objectives:**

The course is taught with the objectives of enabling the student to:

|   |  |
|---|--|
| 1 | To learn concepts of reliability and maintainability of digital circuits |
| 2 | To understand fault tolerant design methods using redundancy             |
| 3 | To familiarize Self checking and their design using Coding techniques    |
| 4 | To understand scan methods in detail                                     |
| 5 | To understand the implementation of BIST                                 |

**Course Outcomes:**

On completion of this course, the student will be able to :

|             |  |
|-------------|--|
| <b>CO-1</b> | Compute various metrics of reliability for CUT                         |
| <b>CO-2</b> | Implement Redundancy techniques to improve fault tolerance in circuits |
| <b>CO-3</b> | Design self checking circuits for Digital circuits                     |
| <b>CO-4</b> | Apply DFT methods to improve testability in circuits                   |
| <b>CO-5</b> | Develop BIST for logic and memory.                                     |

|            | <b>PO1</b> | <b>PO2</b> | <b>PO3</b> | <b>PO4</b> | <b>PO5</b> |
|------------|------------|------------|------------|------------|------------|
| <b>CO1</b> | 2          | 2          | 2          | 2          | 1          |
| <b>CO2</b> | 2          | 2          | 2          | 1          | -          |
| <b>CO3</b> | 2          | 1          | 1          | 1          | -          |
| <b>CO4</b> | 2          | 1          | 1          | 2          | -          |
| <b>CO5</b> | 2          | 2          | 1          | 1          | -          |

Correlation rating: Low / Medium / High: 1 / 2 / 3 respectively.

**UNIT-I**

*Fault Tolerant Design-I: Basic Concepts:* Reliability Concepts, Failure & Faults, Reliability and Failure rate, Relation between Reliability and Mean time between failure, Maintainability and Availability, Reliability of series, parallel and Parallel – Series combinational circuits.

**UNIT-II**

*Fault Tolerant Design-II :* Basic Concepts – Static, dynamic, hybrid Triple Modular Redundant System, Self-purging redundancy, Siftout redundancy (SMR), 5 MR Re-Configuration techniques, Time redundancy and software redundancy

**UNIT-III**

*Self-Checking Circuits & Fail-Safe Design:* Self Checking circuits: Basic concepts of self-checking circuits, Design of Totally self-checking checker, checkers using m out of n codes, Berger code, Low Cost residue code. Fail Safe Design: Strongly fault secure circuits, fail safe design of sequential circuits using partition theory and Berger code, totally self-checking PLA Design.

**UNIT-IV**

*Digital DFT and scan design:* Ad-hoc DFT methods, scan design – design rules – tests for scan circuits- Multiple scan registers – overheads of scan design – Design automation – Physical design verification and timing verification of scan, Partial scan design and variations of scan

**UNIT – V**

*Built In Self-Test (BIST):* BIST concepts, Random logic BIST-BIST process- Pattern generation – Response compaction – BILBO-Circular self test path system- Circuit initialization- Test point insertion.

Memory BIST: definitions- March test SRAM BIST-SRAM test with MISR – Neighborhood pattern sensitive fault test DRAM BIST-Transparent memory BIST tests, Delay fault BIST.

**SUGGESTED READING:**

|   |  |
|---|--|
| 1 | Parag.K. Lala, "Fault Tolerant and fault testable hardware design", BS publications, 2002.   |
| 2 | M.L. Bushnell and V.D. Agrawal, "Essentials of Electronic Testing for Digital, Memory and Mixed-Signal VLSI Circuits", Kluwer Academic Publishers, 2002. |
| 3 | M. Abramovici, M.A. Breuer and A.D. Friedman, "Digital Systems testing and Testable Design", Jaico Publishing House, 2002.                               |



|                       |                             |          |     |   |          |   |
|-----------------------|-----------------------------|----------|-----|---|----------|---|
| EC142                 | REAL TIME OPERATING SYSTEMS |          |     |   |          |   |
| Program Elective - IV |                             |          |     |   |          |   |
| Pre-requisites        | -                           |          | L   | T | P        | C |
|                       |                             |          | 3   | - | -        | 3 |
| Evaluation            | SEE                         | 60 Marks | CIE |   | 40 Marks |   |

|  |  |
|--|--|
| <b>Course Objectives:</b>  |  |
| The course is taught with the objectives of enabling the student to: |  |
| 1  | To learn basics of Operating Systems.          |
| 2  | To get acquainted with UNIX and POSIX          |
| 3  | To get familiarized with RTOS concepts         |
| 4  | To get acquainted with various commercial RTOS |

|   |  |
|---|--|
| <b>Course Outcomes:</b>                                     |  |
| On completion of this course, the student will be able to : |  |
| <b>CO-1</b>   | Understand the fundamental structure of Operating System.                                    |
| <b>CO-2</b>   | Compare between Hard and Soft Real time systems and analyze real time scheduling algorithms. |
| <b>CO-3</b>   | Analyze the concept of Real Time Kernel and implementation of Inter Process Communication.   |
| <b>CO-4</b>   | Analyze the features of VxWorks and compare the commercially available RTOS's.               |
| <b>CO-5</b>   | Explore the features of UNIX operating system and differentiate between UNIX and POSIX.      |

|            | <b>PO1</b> | <b>PO2</b> | <b>PO3</b> | <b>PO4</b> | <b>PO5</b> |
|------------|------------|------------|------------|------------|------------|
| <b>CO1</b> | 3          | -          | 2          | -          | -          |
| <b>CO2</b> | -          | 3          | 1          | 2          | -          |
| <b>CO3</b> | 1          | -          | 2          | 3          | -          |
| <b>CO4</b> | 2          | 3          | -          | 1          | -          |
| <b>CO5</b> | 3          | 1          | 2          | -          | -          |

Correlation rating: Low / Medium / High: 1 / 2 / 3 respectively.

|   |
|---|
| <b>UNIT-I</b>   |
| Brief Review of Unix Operating Systems (Unix Kernel – File system, Concepts of – Process, Concurrent Execution & Interrupts. Process Management – forks & execution. Programming with system calls, Process Scheduling. Shell programming and filters).<br>Portable Operating System Interface (POSIX) – IEEE Standard 1003.13 & POSIX real time profile. POSIX versus traditional Unix signals, overheads and timing predictability. |

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| <b>UNIT-II</b>  |
| Hard versus Soft Real-time systems – examples, Jobs & Processors, Hard and Soft timing constraints, Hard Real-time systems, Soft Real-time systems. Classical Uni-processor |

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|---|
| Scheduling Algorithms – RMS, Preemptive EDF, Allowing for Preemptive and Exclusion Condition. |
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| <b>UNIT–III</b> |
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|--|
| Concept of Embedded Operating Systems, Differences between Traditional OS and RTOS. Real time System Concepts, RTOS Kernel & Issues in Multitasking – Task Assignment, Task Priorities, Scheduling, Inter task Communication & Synchronization – Definition of Context Switching, Foreground ISRs and Background Tasks. Critical Section – Reentrant Functions, Inter process Communication (IPC) – IPC through Semaphores, Mutex, Mailboxes, Message Queues or Pipes and Event Flags. |
|--|

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| <b>UNIT-IV</b> |
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|--|
| VxWorks – POSIX Real Time Extensions, timeout features, Task Creation, Semaphores (Binary, Counting), Mutex, Mailbox, Message Queues, Memory Management – Virtual to Physical Address Mapping. Comparison of RTOS – VxWorks, $\mu$ C/OS-II and RT Linux for Embedded Applications. |
|--|

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|-----------------|
| <b>UNIT – V</b> |
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|   |
|---|
| Debugging Tools and Cross Development Environment – Software Logic Analyzers, ICEs. Comparison of RTOS – VxWorks, $\mu$ C/OS-II and RT Linux for Embedded Applications. |
|---|

**SUGGESTED READING:**

|   |  |
|---|--|
| 1 | Jane W.S.Liu, <i>Real Time Systems</i> , Pearson Education, Asia, 2001.                    |
| 2 | Wind River Systems, <i>VxWorks Programmers Guide</i> , Wind River Systems Inc.1997.        |
| 3 | Shibu K.V., <i>Introduction to embedded systems</i> , MC Graw-Hill Inc., 1997.             |
| 4 | Tanenbaum, <i>Modern Operating Systems</i> , 3rd edition, Pearson Edition, 2007.           |
| 5 | Jean.J.Labrosse, <i>MicroC/OS-II</i> , The CMP Books.                                      |
| 6 | C.M.Krishna and G.Shin, <i>Real Time System</i> , McGraw Hill International Editions, 1997 |

|                       |                                  |          |     |   |          |   |
|-----------------------|----------------------------------|----------|-----|---|----------|---|
| EC143                 | DSP PROCESSORS AND ARCHITECTURES |          |     |   |          |   |
| Program Elective - IV |                                  |          |     |   |          |   |
| Pre-requisites        | -                                |          | L   | T | P        | C |
|                       |                                  |          | 3   | - | -        | 3 |
| Evaluation            | SEE                              | 60 Marks | CIE |   | 40 Marks |   |

**Course Objectives:**

The course is taught with the objectives of enabling the student to:

|   |  |
|---|--|
| 1 | To give an exposure to the various fixed point DSP architectures |
| 2 | To give an exposure to floating-point DSP architectures          |
| 3 | To develop applications using these processors.                  |

**Course Outcomes:**

On completion of this course, the student will be able to :

|             |  |
|-------------|--|
| <b>CO-1</b> | Identify and formalize architectural level characterization of DSP hardware                              |
| <b>CO-2</b> | Design and implement signal processing modules in DSPs   |
| <b>CO-3</b> | Ability to design, programming (assembly and C), and testing code using Code Composer Studio environment |
| <b>CO-4</b> | Deployment of DSP hardware for Control, Audio and Video Signal processing Applications                   |
| <b>CO-5</b> | Understanding of major areas and challenges in DSP based embedded systems.                               |

|            | <b>PO1</b> | <b>PO2</b> | <b>PO3</b> | <b>PO4</b> | <b>PO5</b> |
|------------|------------|------------|------------|------------|------------|
| <b>CO1</b> | 1          | 2          | 1          | 2          | -          |
| <b>CO2</b> | 2          | 3          | 2          | 1          | -          |
| <b>CO3</b> | 1          | 3          | 3          | 3          | -          |
| <b>CO4</b> | 1          | 3          | 2          | 2          | -          |
| <b>CO5</b> | 1          | 2          | 1          | 1          | -          |

Correlation rating: Low / Medium / High: 1 / 2 / 3 respectively.

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| <b>UNIT-I</b>  |
| Introduction to Digital Signal Processing: Introduction, A Digital signal-processing system, The sampling process, Discrete time sequences. Discrete Fourier Transform (DFT) and Fast Fourier Transform (FFT), Linear time-invariant systems, Digital filters, Decimation and interpolation. Computational Accuracy in DSP Implementations: Number formats for signals and coefficients in DSP systems, Dynamic Range and Precision, Sources of error in DSP implementations, A/D Conversion errors, DSP Computational errors, D/A Conversion Errors, Compensating filter. |

**UNIT-II**

Architectures for Programmable DSP Devices: Basic Architectural features, DSP Computational Building Blocks, Bus Architecture and Memory, Data Addressing Capabilities, Address Generation UNIT, Programmability and Program Execution, Speed Issues, Features for External interfacing.

**UNIT-III**

Programmable Digital Signal Processors: Commercial Digital signal-processing Devices, Data Addressing modes of TMS320C54XX DSPs, Data Addressing modes of TMS320C54XX Processors, Memory space of TMS320C54XX Processors, Program Control, TMS320C54XX instructions and Programming, On-Chip Peripherals, Interrupts of TMS320C54XX processors, Pipeline operation of TMS320C54XX Processors.

**UNIT-IV**

Analog Devices Family of DSP Devices: Analog Devices Family of DSP Devices – ALU and MAC block diagram, Shifter Instruction, Base Architecture of ADSP 2100, ADSP2181 high performance Processor. Introduction to Blackfin Processor - The Blackfin Processor, Introduction to Micro Signal Architecture, Overview of Hardware Processing Units and Register files, Address Arithmetic Unit, Control Unit, Bus Architecture and Memory, Basic Peripherals.

**UNIT – V**

Interfacing Memory and I/O Peripherals to Programmable DSP Devices: Memory space organization, External bus interfacing signals, Memory interface, Parallel I/O interface, Programmed I/O, Interrupts and I/O, Direct memory access (DMA).

**SUGGESTED READING:**

|   |   |
|---|---|
| 1 | A Practical Approach to Digital Signal Processing - K Padmanabhan, R. Vijayarajeswaran, Ananthi. S, New Age International, 2006/2009                            |
| 2 | Embedded Signal Processing with the Micro Signal Architecture Publisher: Woon-Seng Gan, Sen M. Kuo, Wiley-IEEE Press, 2007                                      |
| 3 | Digital Signal Processors, Architecture, Programming and Applications – B. Venkataramani and M. Bhaskar, 2002, TMH.   |
| 4 | DSP Processor Fundamentals, Architectures & Features – Lapsley et al. 2000, S. Chand & Co.  |
| 5 | Digital Signal Processing Applications Using the ADSP-2100 Family by The Applications Engineering Staff of Analog Devices, DSP Division, Edited by Amy Mar, PHI |

|                      |                     |          |     |   |          |   |
|----------------------|---------------------|----------|-----|---|----------|---|
| EC151                | SCRIPTING LANGUAGES |          |     |   |          |   |
| Program Elective - V |                     |          |     |   |          |   |
| Pre-requisites       | -                   |          | L   | T | P        | C |
|                      |                     |          | 3   | - | -        | 3 |
| Evaluation           | SEE                 | 60 Marks | CIE |   | 40 Marks |   |

|  |   |
|--|---|
| <b>Course Objectives:</b>  |   |
| The course is taught with the objectives of enabling the student to: |   |
| 1  | To Know Linux Operating System and work with Simple commands. |
| 2  | To learn PERL Scripting language.                             |
| 3  | To learn Python Scripting language.                           |

|   |  |
|---|--|
| <b>Course Outcomes:</b>                                     |  |
| On completion of this course, the student will be able to : |  |
| <b>CO-1</b>   | Use Linux operating system based work stations.          |
| <b>CO-2</b>   | Understand PERL scripting language and its basics.       |
| <b>CO-3</b>   | Describe Regular expressions and Data structures of PERL |
| <b>CO-4</b>   | Understand Python scripting language and its basics      |
| <b>CO-5</b>   | Describe Files, Lists and Strings concept in Python.     |

|            | <b>PO1</b> | <b>PO2</b> | <b>PO3</b> | <b>PO4</b> | <b>PO5</b> |
|------------|------------|------------|------------|------------|------------|
| <b>CO1</b> | 3          | -          | -          | -          | 3          |
| <b>CO2</b> | 3          | -          | -          | -          | 3          |
| <b>CO3</b> | -          | -          | 3          | -          | -          |
| <b>CO4</b> | 3          | -          | -          | -          | 3          |
| <b>CO5</b> | -          | -          | 3          | -          | -          |

Correlation rating: Low / Medium / High: 1 / 2 / 3 respectively.

|   |
|---|
| <b>UNIT-I</b>   |
| <b>Linux Basics:</b> Introduction to Linux, File System of the Linux, General usage of Linux kernel & basic commands, Permissions for file, directory and users, searching a file & directory, zipping and unzipping concepts, Overview of scripting languages. |

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| <b>UNIT-II</b>   |
| <b>PERL Scripting Language:</b> PERL basics, file handles, operators, control structures, regular expressions, built in data types, operators, statements and declarations- simple, compound, loop statements, global and scoped declarations. Lists, Arrays and Hashes. |

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| <b>UNIT-III</b>  |
| Pattern Matching in PERL - Regular expression, Pattern matching operators, character |

classes. PERL Subroutines, Semantics, Prototypes, References, Data Structures- arrays of arrays, hashes of arrays. PERL Debugger, Programs using PERL.

**UNIT-IV**

**Python Scripting Language:** Variables in Python, Performing Calculations (Operators, Type conversions, Expressions), Decision Structures and Boolean Logic, Comparing Strings, Logical Operators, Boolean Variables. Repetition Structures: Introduction, while loop, for loop, Input Validation Loops, Nested Loops.

Functions: Introduction, Defining and Calling a Void Function, Designing a Program to Use Functions, Local Variables, Passing Arguments to Functions, Global Variables and Global Constants, Value-Returning Functions.

**UNIT – V**

**Lists and Tuples:** Sequences, Introduction to Lists, List slicing, Finding Items in Lists with the in Operator, List Methods and Useful Built-in Functions, Copying Lists, Processing Lists, Two-Dimensional Lists, Tuples.

File and Exceptions: Introduction to File Input and Output, Using Loops to Process Files, Processing Records, Exceptions.

Strings: Basic String Operations, String Slicing, Testing, Searching, and Manipulating Strings

**SUGGESTED READING:**

|   |  |
|---|--|
| 1 | Larry Wall, Tom Christiansen, John Orwant, "Programming PERL", Oreilly publications, 3 <sup>rd</sup> ed. |
| 2 | Tony Gaddis, <i>Starting out with Python</i> , Pearson College Division, 3 <sup>rd</sup> Edition, 2014.  |
| 3 | ReemaThareja, "Python programming using problem solving approach", Oxford University Press.              |

|                      |                                 |          |     |   |          |   |
|----------------------|---------------------------------|----------|-----|---|----------|---|
| EC152                | UNMANNED AERIAL VEHICLE SYSTEMS |          |     |   |          |   |
| Program Elective - V |                                 |          |     |   |          |   |
| Pre-requisites       | -                               |          | L   | T | P        | C |
|                      |                                 |          | 3   | - | -        | 3 |
| Evaluation           | SEE                             | 60 Marks | CIE |   | 40 Marks |   |

**Course Objectives :**

The course is taught with the objectives of enabling the student to:

|   |  |
|---|--|
| 1 | To explain and make the students to understand the basic concepts of UAV/DRONE systems and its applications. |
| 2 | To understand the different hardware configurations for UAV.   |
| 3 | To understand the designing, integration and testing of UAV.   |
| 4 | To understand the GCS Software & applications.   |
| 5 | To demonstrate the flight configurations and Practical implementation.                                       |

**Course Outcomes :**

On completion of this course, the student will be able to :

|             |   |
|-------------|---|
| <b>CO-1</b> | Able to identify different hardware for UAV.  |
| <b>CO-2</b> | Prepare preliminary design requirements for an unmanned aerial vehicle.   |
| <b>CO-3</b> | Perform system testing for unmanned aerial vehicles.  |
| <b>CO-4</b> | Integrate various systems of unmanned aerial vehicle.   |
| <b>CO-5</b> | Design micro aerial vehicle systems by considering practical limitations. Understanding of GCS Software & Practical implementation. |

| Course outcome | Program Outcome |      |      |      |      |      |
|----------------|-----------------|------|------|------|------|------|
|                | PO-1            | PO-2 | PO-3 | PO-4 | PO-5 | PO-6 |
| <b>CO-1</b>    | 3               | 3    | 3    | 2    | 1    | 3    |
| <b>CO-2</b>    | 3               | 3    | 3    | 2    | 2    | 3    |
| <b>CO-3</b>    | 3               | 3    | 3    | 2    | 2    | 3    |
| <b>CO-4</b>    | 3               | 3    | 3    | 2    | 1    | 3    |
| <b>CO-5</b>    | 3               | 3    | 3    | 2    | 2    | 3    |

**Chapter – I**

Introduction to Unmanned Aerial Vehicle Systems -- evolution of UAV – classification – models and prototypes – System Composition-applications.

**Chapter – II**

Introduction to Design and Selection of the System- Aerodynamics and Airframe Configurations- Characteristics of Aircraft Types- Regulations of DGCA- Fixed Wing Operations and Aerodynamics - Drone Piloting-Weather and Meteorology- ATC Procedures & Radio Telephony.

**Chapter – III**

Basic Components of Drone - Different Types of Drones- Assembling of Drone, Artificial Intelligence in Drone -Drone Mapping.

**Chapter – IV**

Theory of Flight-Three Axes of Flight-Take –Off - Landing – Hover- Turning- Forwards and Sideway-Aerodynamic of Drone.

**Chapter –V**

Waypoints Navigation-Introduction to Ground Control software (GCS) - System Ground Testing- System In-flight Testing of Mini and Micro UAVs- Case study on the usage of UAV/DRONE.

**Suggested Reading:**

|   |   |
|---|---|
| 1 | Paul G Fahlstrom, Thomas J Gleason, “Introduction to UAV Systems”, UAV Systems, Inc, 1998.                              |
| 2 | Reg Austin “Unmanned Aircraft Systems UAV design, development and deployment”, Wiley, 2010.                             |
| 3 | Dr. Armand J. Chaput, “Design of Unmanned Air Vehicle Systems”, Lockheed Martin Aeronautics Company, 2001.              |
| 4 | Kimion P. Valavanis, “Advances in Unmanned Aerial Vehicles: State of the Art and the Road to Autonomy”, Springer, 2007. |
| 5 | Robert C. Nelson, Flight Stability and Automatic Control, McGraw-Hill, Inc, 1998.                                       |



|                      |                       |          |     |   |          |   |
|----------------------|-----------------------|----------|-----|---|----------|---|
| EC153                | LOW POWER VLSI DESIGN |          |     |   |          |   |
| Program Elective - V |                       |          |     |   |          |   |
| Pre-requisites       | -                     |          | L   | T | P        | C |
|                      |                       |          | 3   | - | -        | 3 |
| Evaluation           | SEE                   | 60 Marks | CIE |   | 40 Marks |   |

**Course Objectives:**

The course is taught with the objectives of enabling the student to:

|   |  |
|---|--|
| 1 | To study the sources of power dissipation and low power design techniques with voltage |
| 2 | To study various low power arithmetic units and the design of low power multipliers    |
| 3 | To study about low power memory technologies   |

**Course Outcomes:**

On completion of this course, the student will be able to :

|             |  |
|-------------|--|
| <b>CO-1</b> | Understand various power components  |
| <b>CO-2</b> | Understand and design low power memories                                   |
| <b>CO-3</b> | Understand and use mathematical models for power analysis in CMOS circuits |
| <b>CO-4</b> | Design low power architectures   |
| <b>CO-5</b> | Understand and design multipliers  |

|            | <b>PO1</b> | <b>PO2</b> | <b>PO3</b> | <b>PO4</b> | <b>PO5</b> |
|------------|------------|------------|------------|------------|------------|
| <b>CO1</b> | 1          | 2          | 1          | 1          | 1          |
| <b>CO2</b> | 2          | 2          | 1          | 1          | 1          |
| <b>CO3</b> | 2          | 2          | 1          | 1          | 1          |
| <b>CO4</b> | 1          | 2          | 1          | 1          | 1          |
| <b>CO5</b> | 2          | 2          | 1          | 1          | 1          |

Correlation rating: Low / Medium / High: 1 / 2 / 3 respectively.

|   |
|---|
| <b>UNIT-I</b>   |
| <i>Fundamentals:</i> Need for Low Power Circuit Design, Sources of Power Dissipation – Switching Power Dissipation, Short Circuit Power Dissipation, Leakage Power Dissipation, Glitching Power Dissipation, Short Channel Effects – Drain Induced Barrier Lowering and Punch Through, Surface Scattering, Velocity Saturation, Impact Ionization, Hot Electron Effect. |

|   |
|---|
| <b>UNIT-II</b>  |
| <i>Low-Power Design Approaches:</i> Low-Power Design through Voltage Scaling – VTCMOS circuits, MTCMOS circuits, Architectural Level Approach – Pipelining and Parallel Processing Approaches. Switched Capacitance, Minimization Approaches: System Level Measures and Circuit Level Measures. |

**UNIT-III**

*Low-Voltage Low-Power Adders:* Introduction, Standard Adder Cells, CMOS Adder Architectures – Ripple Carry Adders, Carry Look-Ahead Adders, Carry Select Adders, Carry Save Adders, Low-Voltage Low-Power Design Techniques–Trends of Technology and Power Supply Voltage, Low-Voltage Low-Power Logic Styles.

**UNIT-IV**

*Low-Voltage Low-Power Multipliers:* Introduction, Overview of Multiplication, Types of Multiplier Architectures, Braun Multiplier, Baugh-Wooley Multiplier, Booth Multiplier and Introduction to Wallace Tree Multiplier.

**UNIT – V**

*Low-Voltage Low-Power Memories:* Basics of ROM, Low-Power ROM Technology, Future Trend and Development of ROMs, Basics of SRAM, Memory Cell, Pre-charge and Equalization Circuit, Low-Power SRAM Technologies, Basics of DRAM, Self-Refresh Circuit, Future Trend and Development of DRAM.

**SUGGESTED READING:**

|   |  |
|---|--|
| 1 | Sung-Mo Kang, Yusuf Leblebici, “ <i>CMOS Digital Integrated Circuits – Analysis and Design</i> ”, TMH, 2011.   |
| 2 | Ming-BO Lin, “ <i>Introduction to VLSI Systems: A Logic, Circuit and System Perspective</i> ”, CRC Press, 2011 |
| 3 | AnanthaChandrakasan, “ <i>Low Power CMOS Design</i> ”, IEEE Press/Wiley International, 1998                    |
| 4 | Kaushik Roy, Sharat C. Prasad, “ <i>Low Power CMOS VLSI Circuit Design</i> ”, John Wiley & Sons, 2000.         |
| 5 | Gary K. Yeap, “ <i>Practical Low Power Digital VLSI Design</i> ”, Kluwer Academic Press, 2002.                 |

|                 |                           |          |     |   |          |   |
|-----------------|---------------------------|----------|-----|---|----------|---|
| OE 941 BM       | MEDICAL ASSISTIVE DEVICES |          |     |   |          |   |
| (OPEN ELECTIVE) |                           |          |     |   |          |   |
| Pre-requisites  |                           |          | L   | T | P        | C |
|                 |                           |          | 3   | - | -        | 3 |
| Evaluation      | SEE                       | 60 Marks | CIE |   | 40 Marks |   |

|  |  |
|--|--|
| <b>Course Objectives :</b>   |  |
| The course is taught with the objectives of enabling the student to: |  |
| 1  | To extend knowledge of the amputee, of lost and remaining functions affecting Locomotion, and to collect information on the best possible medical treatment. |
| 2  | To improve fitting techniques and practices, including training, so that existing devices might be used with greater comfort and function                    |
| 3  | To develop improved lower-extremity devices  |

|   |  |
|---|--|
| <b>Course Outcomes :</b>                                    |  |
| On completion of this course, the student will be able to : |  |
| <b>CO-1</b>   | Apply fundamental knowledge of engineering in rehabilitation                           |
| <b>CO-2</b>   | Apply analytical skills to assess and evaluate the need of the end-user                |
| <b>CO-3</b>   | Develop self-learning initiatives and integrate learned knowledge for problem solving  |
| <b>CO-4</b>   | Understand the basics of robotics and apply their principles in developing prosthetics |
| <b>CO-5</b>   | Apply the knowledge of computers in solving rehabilitation problems                    |

| Course Outcome | Program Outcome |      |      |      |      |
|----------------|-----------------|------|------|------|------|
|                | PO-1            | PO-2 | PO-3 | PO-4 | PO-5 |
| <b>CO-1</b>    | 3               | -    | 2    | -    | -    |
| <b>CO-2</b>    | 3               | 3    | 3    | -    | -    |
| <b>CO-3</b>    | 3               | 3    | 3    | -    | -    |
| <b>CO-4</b>    | 3               | -    | 3    | -    | -    |
| <b>CO-5</b>    | -               | -    | 3    | -    | -    |

|   |
|---|
| <b>UNIT – I</b>   |
| Introduction to Rehabilitation Engineering, Measurement and analysis of human movement, Disability associated with aging in the workplace and their solutions, clinical practice of rehabilitation engineering. |

|   |
|---|
| <b>UNIT – II</b>  |
| Assistive Technology, Seating Biomechanics and systems. Wheeled Mobility: Categories of Wheelchairs. Wheelchair Structure and Component Design. Ergonomics of Wheel chair propulsion. Power Wheelchair Electrical Systems. Control. Personal Transportation. Auxiliary devices and systems. |

**UNIT – III**

Sensory augmentation and substitution: Visual system: Visual augmentation. Tactual vision substitution, Auditory vision substitution; Auditory system: Auditory augmentation. Cochlear implantation, Visual auditory substitution, Tactual auditory substitution, Tactual system: Tactual augmentation. Tactual substitution. Measurement tools and processes: Fundamental principles, structure, function; performance and behaviour. Subjective and objective measurement methods.

**UNIT – IV**

Rehabilitation Robotics, Major Limb Prosthetic Devices, Orthotic Devices, Types of orthotics and prosthetics, Intelligent prosthetic Knee, Prosthetic Hand, Controlled orthotics and prosthetics FES system, Restoration of Hand function, Restoration of standing and walking, Myo-electric Hand.

**UNIT –V**

Augmentative and Alternative communication technology, Computer applications in Rehabilitation Engineering, telecommunications, and Web Accessibility.

**Suggested Reading:**

|   |   |
|---|---|
| 1 | Robinson C.J., Rehabilitation Engineering, CRC Press, 1995.   |
| 2 | Ballabio E., et al., Rehabilitation Technology, IOS Press, 1993.  |
| 3 | Rory A Cooper, Hisaichi Ohnabe, Douglas A. Hobson, Series in medical physics and biomedical engineering: An introduction to rehabilitation engineering, Taylor and Francis Group, London, 2007. |
| 4 | Joseph D. Bronzino The biomedical engineering handbook -biomedical engineering Fundamentals, 3 <sup>rd</sup> Ed., CRC Press, Taylor & Francis Group, London, 2006.                              |

| OE 942 BM       | MEDICAL IMAGING TECHNIQUES |          |     |   |          |   |
|-----------------|----------------------------|----------|-----|---|----------|---|
| (OPEN ELECTIVE) |                            |          |     |   |          |   |
| Pre-requisites  |                            |          | L   | T | P        | C |
|                 |                            |          | 3   | - | -        | 3 |
| Evaluation      | SEE                        | 60 Marks | CIE |   | 40 Marks |   |

| Course Objectives :  |  |
|--|--|
| The course is taught with the objectives of enabling the student to: |  |
| 1  | To familiarize the students with various medical imaging modalities.   |
| 2  | To make learners understand the principles, detectors and operating procedures of X-ray, CT, MRI, ultrasound, PET and SPECT. |
| 3  | To make the students learn the advantages, disadvantages and hazards of various medical imaging equipment.                   |

| Course Outcomes :   |  |
|---|--|
| On completion of this course, the student will be able to : |  |
| CO-1  | Interpret the working principle and operating procedure and applications of X-ray equipment. |
| CO-2  | Understand the image reconstruction techniques and applications of CT.                       |
| CO-3  | Summarize the image acquisition and reconstruction techniques in MRI.                        |
| CO-4  | Comprehend the working principle, modes and medical applications of ultrasound imaging.      |
| CO-5  | Examine the operation and applications of PET, SPECT and radio nuclide instrumentation.      |

| Course Outcome | Program Outcome |      |      |      |      |
|----------------|-----------------|------|------|------|------|
|                | PO-1            | PO-2 | PO-3 | PO-4 | PO-5 |
| CO-1           | -               | -    | 3    | -    | -    |
| CO-2           | 2               | -    | 3    | -    | -    |
| CO-3           | -               | 3    | -    | -    | -    |
| CO-4           | 3               | -    | 3    | -    | -    |
| CO-5           | 3               | -    | -    | -    | -    |

| UNIT – I  |
|---|
| X ray Imaging: Electromagnetic spectrum, Production of X-rays, X-ray tubes- Stationary and Rotating Anode types, Block diagram of an X-Ray Machine, Collimators and Grids, Timing and Exposure controls. X-Ray Image visualization-Films, Fluorescent screens, Image Intensifiers. Dental X-Ray machines, Portable and mobile X-Ray UNITS, Mammographic X-Ray equipment, Digital Radiography and flat panel detectors. Radiation safety, ALARA principle, Dose UNITS and dose limits, Radiation dosimeters and detectors. |

| UNIT – II  |
|--|
| Computed Tomography: Basic principles, CT number scale, CT Generations. Major sub systems- Scanning system, processing UNIT, viewing UNIT, storage UNIT. Need and Principle of sectional |

imaging, 2D image reconstruction techniques - Iteration and Fourier methods. Applications of CT - Angio, Osteo, Dental, Perfusion (Body & Neuro), Virtual Endoscopy, Coronary Angiography.

### UNIT – III

Magnetic Resonance Imaging: Principles of NMR imaging systems, Image reconstruction techniques-Relaxation processes, imaging/ pulse sequences. Sub systems of an NMR imaging system, NMR detection system, types of coils, biological effects and advantages of NMR imaging.  
Functional MRI - The BOLD effect, intra and extra vascular field offsets, source of T2\* effects, Creating BOLD contrast sequence optimization sources and dependences of physiological noise in fMRI.

### UNIT – IV

Ultrasound Imaging: - Principles of image formation -Imaging principles and instrumentation of A-mode, B-Mode, Gating Mode, Transmission mode and M-mode. Basics of multi-element linear array scanners, Digital scan conversion.  
Doppler Ultrasound and Colour Doppler imaging, Image artifacts, Biological effects, Ultrasound applications in diagnosis, therapy and surgery.

### UNIT –V

Nuclear Medicine–Radioisotopes in medical diagnosis, Basic instrumentation- Radiation detectors, Pulse height analyzer, Rectilinear scanner, Gamma camera.  
Emission Computed Tomography (ECT), Principle and instrumentation of Single Photon Emission Computed Tomography (SPECT) and Positron Emission Tomography (PET).  
Comparison of SPECT, PET and combined PET/ X-ray CT.

### Suggested Reading:

|   |   |
|---|---|
| 1 | Khandpur R.S., Handbook of Biomedical Instrumentation, Tata McGraw Hill, 2016.                            |
| 2 | S Webb, "The Physics of Medical Imaging", Adam Highler, Bristol Published by CRC Press, 1988.             |
| 3 | A C Kak, "Principle of Computed Tomography", IEEE Press New York, 1988.                                   |
| 4 | Hykes, Heorick, Starchman, Ultrasound physics and Instrumentation MOSBY year book, 2ndEd. 1992.           |
| 5 | Stewart C. Bushong, Magnetic Resonance Imaging- physical and biological principles, MOSBY, 2nd Ed., 1995. |

|                 |                           |          |     |   |          |   |
|-----------------|---------------------------|----------|-----|---|----------|---|
| OE 941 CE       | GREEN BUILDING TECHNOLOGY |          |     |   |          |   |
| (OPEN ELECTIVE) |                           |          |     |   |          |   |
| Pre-requisites  |                           |          | L   | T | P        | C |
|                 |                           |          | 3   | - | -        | 3 |
| Evaluation      | SEE                       | 60 Marks | CIE |   | 40 Marks |   |

|  |   |
|--|---|
| <b>Course Objectives :</b>   |   |
| The course is taught with the objectives of enabling the student to: |   |
| 1  | Exposure to the green building technologies and their significance. |
| 2  | Understand the judicious use of energy and its management.          |
| 3  | Educate about the Sun-earth relationship and its effect on climate. |
| 4  | Enhance awareness of end-use energy requirements in the society.    |
| 5  | Develop suitable technologies for energy management                 |

|   |   |
|---|---|
| <b>Course Outcomes :</b>                                    |   |
| On completion of this course, the student will be able to : |   |
| <b>CO-1</b>   | Understand the fundamentals of energy use and energy processes in building. |
| <b>CO-2</b>   | Identify the energy requirement and its management.                         |
| <b>CO-3</b>   | Know the Sun-earth relationship vis-a-vis its effect on climate.            |
| <b>CO-4</b>   | Be acquainted with the end-use energy requirements.                         |
| <b>CO-5</b>   | Be familiar with the audit procedures of energy                             |

| Course Outcome | Program Outcome |      |      |      |      |
|----------------|-----------------|------|------|------|------|
|                | PO-1            | PO-2 | PO-3 | PO-4 | PO-5 |
| <b>CO-1</b>    | 3               | -    | -    | -    | -    |
| <b>CO-2</b>    | 3               | -    | 2    | -    | -    |
| <b>CO-3</b>    | 3               | -    | 3    | -    | -    |
| <b>CO-4</b>    | 3               | -    | 3    | -    | -    |
| <b>CO-5</b>    | -               | 3    | -    | -    | -    |

|  |
|--|
| <b>UNIT – I</b>  |
| Overview of the significance of energy use and energy processes in building - Indoor activities and environmental control - Internal and external factors on energy use and the attributes of the factors - Characteristics of energy use and its management - Macro aspect of energy use in dwellings and its implications. |

|   |
|---|
| <b>UNIT – II</b>  |
| Indoor environmental requirement and management - Thermal comfort - Ventilation and air quality – Air-conditioning requirement - Visual perception - Illumination requirement - Auditory requirement. |

|                   |
|-------------------|
| <b>UNIT – III</b> |
|-------------------|

Climate, solar radiation and their influences - Sun-earth relationship and the energy balance on the earth's surface - Climate, wind, solar radiation, and temperature - Sun shading and solar radiation on surfaces - Energy impact on the shape and orientation of buildings.

**UNIT – IV**

End-use, energy utilization and requirements - Lighting and day lighting - End-use energy requirements - Status of energy use in buildings Estimation of energy use in a building. Heat gain and thermal performance of building envelope - Steady and non-steady heat transfer through the glazed window and the wall - Standards for thermal performance of building envelope - Evaluation of the overall thermal transfer.

**UNIT –V**

Energy management options - Energy audit and energy targeting - Technological options for energy management.

**Suggested Reading:**

|   |  |
|---|--|
| 1 | Bryant Edwards (2005): Natural Hazards, Cambridge University Press, U.K.   |
| 2 | Carter, W. Nick, (1991): Disaster Management, Asian Development Bank, Manila.  |
| 3 | Sahni, Pardeep et.al. (eds.) (2002), Disaster Mitigation Experiences and Reflections, Prentice Hall of India, New Delhi. |
| 4 | Bryant Edwards (2005): Natural Hazards, Cambridge University Press, U.K.   |



|                 |   |          |     |   |          |   |
|-----------------|---|----------|-----|---|----------|---|
| OE 942 CE       | COST MANAGEMENT OF ENGINEERING PROJECTS |          |     |   |          |   |
| (OPEN ELECTIVE) |   |          |     |   |          |   |
| Pre-requisites  |   |          | L   | T | P        | C |
|                 |   |          | 3   | - | -        | 3 |
| Evaluation      | SEE                                     | 60 Marks | CIE |   | 40 Marks |   |

|  |   |
|--|---|
| <b>Course Objectives :</b>   |   |
| The course is taught with the objectives of enabling the student to: |   |
| 1  | Introduce the concepts of cost management   |
| 2  | Fundamentals of cost overruns   |
| 3  | Introduce the concepts of Quantitative techniques for cost management Linear Programming, PERT/CPM. |

|   |   |
|---|---|
| <b>Course Outcomes :</b>                                    |   |
| On completion of this course, the student will be able to : |   |
| <b>CO-1</b>   | Understanding of strategic cost management process, control of cost and decision making based on the cost of the project. |
| <b>CO-2</b>   | Ability to appreciate detailed engineering activities of the project and execution of projects                            |
| <b>CO-3</b>   | Preparation of project report and network diagram   |
| <b>CO-4</b>   | Able to plan Cost Behavior , Profit Planning , Enterprise Resource Planning, Total Quality Management.                    |
| <b>CO-5</b>   | Applications of various quantitative techniques for cost management   |

| Course Outcome | Program Outcome |      |      |      |      |
|----------------|-----------------|------|------|------|------|
|                | PO-1            | PO-2 | PO-3 | PO-4 | PO-5 |
| <b>CO-1</b>    | 3               | -    | -    | -    | -    |
| <b>CO-2</b>    | 3               | -    | 3    | -    | -    |
| <b>CO-3</b>    | -               | 3    | -    | -    | -    |
| <b>CO-4</b>    | -               | 3    | -    | -    | -    |
| <b>CO-5</b>    | 3               | -    | -    | -    | -    |

|   |
|---|
| <b>UNIT – I</b>   |
| Introduction and Overview of the Strategic Cost Management Process-Cost concepts in decision-making; relevant cost, Differential cost, Incremental cost and Opportunity cost. Objectives of a Costing System- Inventory valuation- Creation of a Database for operational control; Provision of data for Decision-Making. |

|   |
|---|
| <b>UNIT – II</b>  |
| Project: meaning, Different types, why to manage, cost overruns centres, various stages of project execution: conception to commissioning- Project execution as conglomeration of technical and non- technical activities- Detailed Engineering activities. |

**UNIT – III**

Pre project execution main clearances and documents Project team: Role of each member. Importance Project site: Data required with significance. Project contracts. Types and contents. Project execution Project cost control. Bar charts and Network diagram. Project commissioning: mechanical and process

**UNIT – IV**

Cost Behavior and Profit Planning Marginal Costing; Distinction between Marginal Costing and Absorption Costing; Break-even Analysis, Cost-Volume-Profit Analysis. Various decision-making problems- Standard Costing and Variance Analysis. Pricing strategies: Pareto Analysis. Target costing, Life Cycle Costing. Costing of service sector- Just-in-time approach, Material Requirement Planning, Enterprise Resource Planning, Total Quality Management and Theory of constraints- Activity-Based Cost Management, Bench Marking; Balanced Score Card and Value-Chain Analysis. Budgetary Control; Flexible Budgets- Performance budgets- Zero-based budgets. Measurement of Divisional profitability pricing decisions including transferpricing.

**UNIT –V**

Quantitative techniques for cost management, Linear Programming, PERT/CPM,- Transportation problems, Assignment problems, Simulation, Learning Curve Theory.

**Suggested Reading:**

|   |  |
|---|--|
| 1 | Cost Accounting A Managerial Emphasis, Prentice Hall of India, NewDelhi                  |
| 2 | Charles T. Horngren and George Foster, Advanced ManagementAccounting                     |
| 3 | Robert S Kaplan Anthony A. Alkinson, Management &CostAccounting                          |
| 4 | Ashish K. Bhattacharya, Principles & Practices of Cost Accounting A. H. Wheelerpublisher |
| 5 | N.D. Vohra, Quantitative Techniques in Management, Tata McGraw Hill Book Co.Ltd.         |

| OE 941 CS       |     | BUSINESS ANALYTICS |     |   |          |   |
|-----------------|-----|--------------------|-----|---|----------|---|
| (OPEN ELECTIVE) |     |                    |     |   |          |   |
| Pre-requisites  |     |                    | L   | T | P        | C |
|                 |     |                    | 3   | - | -        | 3 |
| Evaluation      | SEE | 60 Marks           | CIE |   | 40 Marks |   |

| Course Objectives :  |  |
|--|--|
| The course is taught with the objectives of enabling the student to: |  |
| 1  | Understanding the basic concepts of business analytics and applications                                |
| 2  | Study various business analytics methods including predictive, prescriptive and prescriptive analytics |
| 3  | Prepare the students to model business data using various data mining, decision making methods         |

| Course Outcomes :   |   |
|---|---|
| On completion of this course, the student will be able to : |   |
| CO-1  | To understand the basic concepts of business analytics                                |
| CO-2  | Identify the application of business analytics and use tools to analyze business data |
| CO-3  | Become familiar with various metrics, measures used in business analytics             |
| CO-4  | Illustrate various descriptive, predictive and prescriptive methods and techniques    |
| CO-5  | Model the business data using various business analytical methods and techniques      |

| Course Outcome | Program Outcome |      |      |      |      |
|----------------|-----------------|------|------|------|------|
|                | PO-1            | PO-2 | PO-3 | PO-4 | PO-5 |
| CO-1           | 3               | -    | -    | -    | -    |
| CO-2           | 2               | -    | 3    | -    | -    |
| CO-3           | 3               | -    | -    | -    | -    |
| CO-4           | -               | 3    | -    | -    | -    |
| CO-5           | 3               | -    | 3    | -    | -    |

| UNIT – I  |
|---|
| <b>Introduction to Business Analytics:</b> Introduction to Business Analytics, need and science of data driven (DD) decision making, Descriptive, predictive, prescriptive analytics and techniques, Big data analytics, Web and Social media analytics, Machine Learning algorithms, framework for decision making, challenges in DD decision making and future. |

| UNIT – II  |
|--|
| <b>Descriptive Analytics:</b> Introduction, data types and scales, types of measurement scales, population and samples, measures of central tendency, percentile, decile and quadrille, measures of variation, measures of shape-skewness, data visualization. |

**UNIT – III**

**Forecasting Techniques:** Introduction, time-series data and components, forecasting accuracy, moving average method, single exponential smoothing, Holt's method, Holt-Winter model, Croston's forecasting method, regression model for forecasting, Auto regression models, auto-regressive moving process, ARIMA, Theil's coefficient

**UNIT – IV**

**Decision Trees:** CHAID, Classification and Regression tree, splitting criteria, Ensemble and method and random forest. **Clustering:** Distance and similarity measures used in clustering, Clustering algorithms, K-Means and Hierarchical algorithms, **Prescriptive Analytics-** Linear Programming(LP) and LP model building.

**UNIT –V**

**Six Sigma:** Introduction, introduction, origin, 3-Sigma Vs Six-Sigma process, cost of poor quality, sigma score, industry applications, six sigma measures, DPMO, yield, sigma score, DMAIC methodology, Six Sigma toolbox.

**Suggested Reading:**

|   |  |
|---|--|
| 1 | U Dinesh Kumar, "Data Analytics", Wiley Publications, 1st Edition, 2017  |
| 2 | Marc J. Schniederjans, Dara G. Schniederjans, Christopher M. Starkey, "Business analytics Principles, Concepts, and Applications with SAS", Associate Publishers, 2015 |
| 3 | S. Christian Albright, Wayne L. Winston, "Business Analytics - Data Analysis and Decision Making", 5th Edition, Cengage, 2015  |

**Web Resources:**

|   |   |
|---|---|
| 1 | <a href="https://onlinecourses.nptel.ac.in/noc18-mgl1/preview">https://onlinecourses.nptel.ac.in/noc18-mgl1/preview</a> |
| 2 | <a href="https://nptel.ac.in/courses/110105089/">https://nptel.ac.in/courses/110105089/</a>                             |

| OE 941 EC       | Elements of Embedded Systems |          |     |   |          |   |
|-----------------|------------------------------|----------|-----|---|----------|---|
| (OPEN ELECTIVE) |                              |          |     |   |          |   |
| Pre-requisites  |                              |          | L   | T | P        | C |
|                 |                              |          | 3   | - | -        | 3 |
| Evaluation      | SEE                          | 60 Marks | CIE |   | 40 Marks |   |

| Course Objectives :  |   |
|--|---|
| The course is taught with the objectives of enabling the student to: |   |
| 1  | Understanding various Embedded Design strategies  |
| 2  | Designing Micro controller based Embedded Systems |
| 3  | Designing FPGA Based Embedded Systems             |

| Course Outcomes :   |   |
|---|---|
| On completion of this course, the student will be able to : |   |
| CO-1  | Understand Embedded Design Strategies and architecture of Arduino Board |
| CO-2  | Program using various onboard components of Arduino                     |
| CO-3  | Design real time interfacing with Arduino                               |
| CO-4  | Understand Design Flow of FPGA, programming FPGA using Verilog HDL      |
| CO-5  | Implement combinational and sequential circuits using verilog HDL       |

| Course Outcome | Program Outcome |      |      |      |      |
|----------------|-----------------|------|------|------|------|
|                | PO-1            | PO-2 | PO-3 | PO-4 | PO-5 |
| CO-1           | 3               | -    | -    | -    | -    |
| CO-2           | 3               | -    | 3    | -    | -    |
| CO-3           | 3               | -    | 3    | -    | -    |
| CO-4           | 3               | -    | -    | -    | -    |
| CO-5           | -               | -    | 3    | -    | -    |

| UNIT – I   |
|--|
| Embedded Systems Design Strategies: Micro Controller, DSP, FPGA, Introduction to Arduino (Micro controller Board), Components of Arduino, Architecture and Pin Configuration of ATmega328, Ports of ATmega328. |

| UNIT – II   |
|---|
| Interfacing: Interfacing Switches, LEDs, Analog to Digital Converter, Digital to Analog Converter, Interfacing and Programming I2C, SPI |

| UNIT – III  |
|---|
| Real Time Programming: Interfacing Key Pad, 7-segment display, LCD, Interfacing Sensors, Interfacing Stepper Motor, USB programming |

| UNIT – IV  |
|--|
| FPGA Based Embedded Design: FPGA Design flow, Introduction to Verilog HDL, Basic |

|  |
|--|
| building blocks, Data types of Verilog HDL, Behavioral Modelling, Data Flow Modelling, Structural Modelling, Hierarchical Structural Modelling, Case Studies on Verilog HDL descriptions of Basic Circuits |
|--|

|                |
|----------------|
| <b>UNIT –V</b> |
|----------------|

|   |
|---|
| Modeling of Circuits: Verilog HDL Implementation of Combinational MSI Circuits, Verilog HDL Implementation of Sequential MSI Circuits, Finite State Machine Design, Tasks and Functions, Introduction to Test Benches |
|---|

**Suggested Reading:**

|   |  |
|---|--|
| 1 | Ming-Bo Lin, Digital System Designs and Practices Using Verilog HDL and FPGAs, Wiley India, 2008 |
| 2 | Samir Palnitkar, Verilog HDL: A Guide to Digital Design and Synthesis, Pearson Education, 2005   |
| 3 | Simon Monk, Programming Arduino: Getting Started with sketches, Mc.Hill, 2016                    |

|                 |                 |          |     |   |          |   |
|-----------------|-----------------|----------|-----|---|----------|---|
| OE 941 EE       | WASTE TO ENERGY |          |     |   |          |   |
| (OPEN ELECTIVE) |                 |          |     |   |          |   |
| Pre-requisites  |                 |          | L   | T | P        | C |
|                 |                 |          | 3   | - | -        | 3 |
| Evaluation      | SEE             | 60 Marks | CIE |   | 40 Marks |   |

**Course Objectives :**

The course is taught with the objectives of enabling the student to:

|   |   |
|---|---|
| 1 | To know the various forms of waste                |
| 2 | To understand the processes of Biomass Pyrolysis. |
| 3 | To learn the technique of Biomass Combustion.     |

**Course Outcomes :**

On completion of this course, the student will be able to :

|             |   |
|-------------|---|
| <b>CO-1</b> | Understand the concept of conservation of waste                   |
| <b>CO-2</b> | Identify the different forms of wastage.                          |
| <b>CO-3</b> | Chose the best way for conservation to produce energy from waste. |
| <b>CO-4</b> | Explore the ways and means of combustion of biomass.              |
| <b>CO-5</b> | Develop a healthy environment for the mankind.                    |

| Course Outcome | Program Outcome |      |      |      |      |
|----------------|-----------------|------|------|------|------|
|                | PO-1            | PO-2 | PO-3 | PO-4 | PO-5 |
| <b>CO-1</b>    | 3               | -    | -    | -    | -    |
| <b>CO-2</b>    | 3               | -    | -    | -    | -    |
| <b>CO-3</b>    | 3               | -    | -    | -    | -    |
| <b>CO-4</b>    | 3               | -    | 3    | -    | -    |
| <b>CO-5</b>    | 3               | -    | 3    | -    | -    |

**UNIT – I**

Introduction to Energy from Waste: Classification of waste as fuel – Agro based, Forest residue, Industrial waste - MSW – Conversion devices – Incinerators, gasifiers, digestors

**UNIT – II**

Biomass Pyrolysis: Pyrolysis – Types, slow fast – Manufacture of charcoal – Methods Yields and application – Manufacture of pyrolytic oils and gases, yields and applications.

**UNIT – III**

Biomass Gasification: Gasifiers – Fixed bed system – Downdraft and updraft gasifiers Fluidized bed gasifiers – Design, construction and operation – Gasifier burner arrangement for thermal heating – Gasifier engine arrangement and electrical power – Equilibrium and kinetic consideration in gasifier operation.

**UNIT – IV**

Biomass Combustion: Biomass stoves – Improved chullahs, types, some exotic designs, Fixed bed combustors, Types, inclined grate combustors, Fluidized bed combustors, Design, construction and operation - Operation of all the above biomass combustors.

**UNIT –V**

Biogas: Properties of biogas (Calorific value and composition) - Biogas plant technology and status - Bio energy system - Design and constructional features - Biomass resources and their classification - Biomass conversion processes - Thermo chemical conversion - Direct combustion - biomass gasification - pyrolysis and liquefaction - biochemical conversion anaerobic digestion - Types of biogas Plants – Applications - Alcohol production from biomass Bio diesel production - Urban waste to energy conversion - Biomass energy programme in India.

**Suggested Reading:**

|   |  |
|---|--|
| 1 | Non Conventional Energy, Desai, Ashok V., Wiley Eastern Ltd., 1990.  |
| 2 | Biogas Technology - A Practical Hand Book - Khandelwal, K. C. and Mahdi, S. S., Vol. I & II, Tata McGraw Hill Publishing Co. Ltd., 1983. |
| 3 | Food, Feed and Fuel from Biomass, Challal, D. S., IBH Publishing Co. Pvt. Ltd., 1991.  |
| 4 | Biomass Conversion and Technology, C. Y. WereKo-Brobby and E. B. Hagan, John Wiley & Sons, 1996.   |



|                 |   |          |     |   |          |   |
|-----------------|---|----------|-----|---|----------|---|
| OE 942 EE       | Power Plant Control and Instrumentation |          |     |   |          |   |
| (OPEN ELECTIVE) |   |          |     |   |          |   |
| Pre-requisites  |   |          | L   | T | P        | C |
|                 |   |          | 3   | - | -        | 3 |
| Evaluation      | SEE                                     | 60 Marks | CIE |   | 40 Marks |   |

|  |   |
|--|---|
| <b>Course Objectives :</b>   |   |
| The course is taught with the objectives of enabling the student to: |   |
| 1  | The operation of different types of power plants.   |
| 2  | The basic working principle of instruments for measurement of electrical and Non-electrical quantities like Temperature Pressure flow level measurements. |
| 3  | The instrumentation and protection systems applied in thermal power plant.  |
| 4  | The control techniques employed for the operation of modern power generation plant  |

|   |  |
|---|--|
| <b>Course Outcomes :</b>                                    |  |
| On completion of this course, the student will be able to : |  |
| <b>CO-1</b>   | Explain the different methods of power generation. Along with Piping and Instrumentation diagram of boiler.      |
| <b>CO-2</b>   | Select various measurements involved in power generation for measuring electrical and non-electrical parameters. |
| <b>CO-3</b>   | Identify the different types of analyzers used for scrutinizing boiler steam and water.                          |
| <b>CO-4</b>   | Model different types of controls and control loops in boilers.  |
| <b>CO-5</b>   | Illustrate the methods of monitoring and control of different parameters like speed, vibration of turbines       |

| Course Outcome | Program Outcome |      |      |      |      |
|----------------|-----------------|------|------|------|------|
|                | PO-1            | PO-2 | PO-3 | PO-4 | PO-5 |
| <b>CO-1</b>    | 3               | -    | -    | -    | -    |
| <b>CO-2</b>    | -               | -    | 3    | -    | -    |
| <b>CO-3</b>    | 3               | -    | 2    | -    | -    |
| <b>CO-4</b>    | 2               | -    | 2    | -    | -    |
| <b>CO-5</b>    | -               | 3    | -    | -    | -    |

|  |
|--|
| <b>UNIT – I</b>  |
| Brief survey of methods of power generation, hydro, thermal, nuclear, solar and wind power, importance of instrumentation in power generation, thermal power plants, block diagram, details of boiler processes, Piping and Instrumentation diagram of boiler, cogeneration. |

|   |
|---|
| <b>UNIT – II</b>  |
| Electrical measurements, current, voltage, power, frequency, power factor etc, non-electrical parameters, flow of feed water, fuel, air and steam with correction factor for temperature, steam |

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| pressure and steam temperature, drum level measurement, radiation detector, smoke density measurement, dust monitor. |
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|                   |
|-------------------|
| <b>UNIT – III</b> |
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|   |
|---|
| Flue gas oxygen analyzer: Analysis of impurities in feed water and steam, dissolved oxygen analyzer. Chromatography, pH meter, fuel analyzer, pollution monitoring instruments. |
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| <b>UNIT – IV</b> |
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|--|
| Combustion control, air / fuel ratio control, furnace draft control, drum level control, main steam and reheat steam temperature control, super heater control, air temperature, distributed control system in power plants, interlocks in boiler operation. |
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|-----------------|
| <b>UNIT – V</b> |
|-----------------|

|  |
|--|
| Speed, vibration, shell temperature monitoring and control, steam pressure control, lubricant oil temperature control, cooling system. |
|--|

**Suggested Reading:**

|   |  |
|---|--|
| 1 | Sam G. Dukelow, The Control of Boilers, Instrument Society of America, 2nd Edition, 2010.      |
| 2 | P.K. Nag, „Power Plant Engineering“, Tata McGraw-Hill, 1st Edition, 2001.                      |
| 3 | S.M. Elonka and A.L. Kohal, “Standard Boiler Operations”, Tata McGraw-Hill, 1st Edition, 1994. |
| 4 | R K Jain, “Mechanical and Industrial Measurements”, Khanna Publishers, 1st Edition, 1995.      |
| 5 | E Al Wakil, “Power Plant Engineering”, Tata McGraw-Hill, 1st Edition, 1984.                    |

|                 |                    |          |     |   |          |   |
|-----------------|--------------------|----------|-----|---|----------|---|
| OE 941 ME       | OPERATION RESEARCH |          |     |   |          |   |
| (OPEN ELECTIVE) |                    |          |     |   |          |   |
| Pre-requisites  |                    |          | L   | T | P        | C |
|                 |                    |          | 3   | - | -        | 3 |
| Evaluation      | SEE                | 60 Marks | CIE |   | 40 Marks |   |

|  |  |
|--|--|
| <b>Course Objectives :</b>   |  |
| The course is taught with the objectives of enabling the student to: |  |
| 1  | Introduce the concepts of optimization techniques  |
| 2  | Formulation of LPP models  |
| 3  | Basic concepts of Non-linear programming, Dynamic programming, Game theory are introduced. |

|   |   |
|---|---|
| <b>Course Outcomes :</b>                                    |   |
| On completion of this course, the student will be able to : |   |
| <b>CO-1</b>   | Students should able to apply the dynamic programming to solve problems of discrete and continuous variables. |
| <b>CO-2</b>   | Students should able to apply the concept of non-linear programming   |
| <b>CO-3</b>   | Students should able to carry out sensitivity analysis  |
| <b>CO-4</b>   | Student should able to model the real world problem and simulate it.  |
| <b>CO-5</b>   | Student should able to apply graph theory, competitive models, and game theory simulations.                   |

| Course Outcome | Program Outcome |      |      |      |      |
|----------------|-----------------|------|------|------|------|
|                | PO-1            | PO-2 | PO-3 | PO-4 | PO-5 |
| <b>CO-1</b>    | 3               | -    | -    | -    | -    |
| <b>CO-2</b>    | 3               | -    | 3    | -    | -    |
| <b>CO-3</b>    | 3               | -    | 3    | -    | -    |
| <b>CO-4</b>    | 3               | -    | 3    | -    | -    |
| <b>CO-5</b>    |                 | -    | 3    | -    | -    |

|  |
|--|
| <b>UNIT – I</b>  |
| Optimization Techniques, Model Formulation, models, General L.R Formulation, Simplex Techniques, Sensitivity Analysis, Inventory Control Models. |

|  |
|--|
| <b>UNIT – II</b>   |
| Formulation of a LPP - Graphical solution revised simplex method - duality theory - dual simplex method - sensitivity analysis - parametric programming. |

|   |
|---|
| <b>UNIT – III</b>   |
| Nonlinear programming problem - Kuhn-Tucker conditions min cost flow problem - max flow problem - CPM/PERT. |

**UNIT – IV**

Scheduling and sequencing - single server and multiple server models deterministic inventory models - Probabilistic inventory control models - Geometric Programming.

**UNIT –V**

Competitive Models, Single and Multi-channel Problems, Sequencing Models, Dynamic Programming, Flow in Networks, Elementary Graph Theory, Game Theory Simulation

**Suggested Reading:**

|   |  |
|---|--|
| 1 | H.A. Taha, Operations Research, An Introduction, PHI,2008                                |
| 2 | H.M. Wagner, Principles of Operations Research, PHI, Delhi,1982.                         |
| 3 | J.C. Pant, Introduction to Optimization: Operations Research, Jain Brothers, Delhi,2008. |
| 4 | Hitler Libermann Operations Research: McGraw Hill Pub.2009                               |
| 5 | Pannerselvam, Operations Research: Prentice Hall of India2010.                           |
| 6 | Harvey M Wagner, Principles of Operations Research: Prentice Hall of India2010.          |

| OE 942 ME       | COMPOSITE MATERIALS |          |     |   |          |   |
|-----------------|---------------------|----------|-----|---|----------|---|
| (OPEN ELECTIVE) |                     |          |     |   |          |   |
| Pre-requisites  |                     |          | L   | T | P        | C |
|                 |                     |          | 3   | - | -        | 3 |
| Evaluation      | SEE                 | 60 Marks | CIE |   | 40 Marks |   |

| Course Objectives :  |   |
|--|---|
| The course is taught with the objectives of enabling the student to: |   |
| 1  | <i>Study the concepts of composite construction.</i>  |
| 2  | <i>Learn analysis and designs of composite beams, floors, columns and trusses as per the recommendations of IS codes of practice.</i> |
| 3  | <i>Apply the concepts for design of multi-storey composite buildings.</i>   |
| 4  | <i>Scope of analysis is restricted to skeletal structures subjected to prescribed dynamic loads.</i>                                  |

| Course Outcomes :   |  |
|---|--|
| On completion of this course, the student will be able to : |  |
| <b>CO-1</b>   | <i>Understand the fundamentals of composite construction, and analysis and designs of composite beams.</i> |
| <b>CO-2</b>   | <i>Analyse and design the composite floors</i>   |
| <b>CO-3</b>   | <i>Select suitable materials for composite columns,</i>  |
| <b>CO-4</b>   | <i>Analyse composite trusses and understand connection details.</i>  |
| <b>CO-5</b>   | <i>Analyse and design the multi-storey composite buildings</i>   |

| Course Outcome | Program Outcome |      |      |      |      |
|----------------|-----------------|------|------|------|------|
|                | PO-1            | PO-2 | PO-3 | PO-4 | PO-5 |
| <b>CO-1</b>    | 3               | -    | -    | -    | -    |
| <b>CO-2</b>    | 3               | -    | 3    | -    | -    |
| <b>CO-3</b>    | 3               | -    | 3    | -    | -    |
| <b>CO-4</b>    | 3               | -    | 3    | -    | -    |
| <b>CO-5</b>    | 3               | -    | 3    | -    | -    |

| UNIT – I  |
|---|
| Introduction of composite constructions: Benefits of composite construction - Introduction to IS - BS and Euro codal provisions.<br>Composite beams: Elastic behaviour of composite beams - No and full interaction cases - Shear connectors - Ultimate load behaviour - Serviceability limits - Effective breadth of flange - Interaction between shear and moment - Basic design consideration and design of composite beams. |

| UNIT – II  |
|--|
| Composite floors: Structural elements - Profiled sheet decking - Bending resistance - Shear resistance - Serviceability criterion - Analysis for internal forces and moments - Design of composite floors. |

**UNIT – III**

Composite columns: Materials - Concrete filled circular tubular sections - Non-dimensional slenderness - Local buckling of steel sections - Effective elastic flexural stiffness - Resistance of members to axial compressions - Composite column design - Fire resistance.

**UNIT – IV**

Composite trusses: Design of truss - Configuration - Truss members - Analysis and design of composite trusses and connection details.

**UNIT – V**

Design of multi-storey composite buildings: Design basis - Load calculations - Design of composite slabs with profile decks - Composite beam design - Design for compression members - Vertical cross bracings - Design of foundation.

**Suggested Reading:**

|   |  |
|---|--|
| 1 | R.P. Johnson, “Composite Structures of Steel and Concrete - Beams, Slabs, Columns and Frames in Buildings”, Blackwell Publishing, Malden, USA, 2004. |
| 2 | “INSDAG Teaching Resources for Structural Steel Design”, Vol-2, Institute for Steel Development and Growth Publishers, Calcutta, India.              |
| 3 | “INSDAG Handbook on Composite Construction – Multi-Storey Buildings”, Institute for Steel Development and Growth Publishers, Calcutta, India.        |
| 4 | “INSDAG Design of Composite Truss for Building”, Institute for Steel Development and Growth Publishers, Calcutta, India.                             |
| 5 | “INSDAG Handbook on Composite Construction – Bridges and Flyovers”, Institute for Steel Development and Growth Publishers, Calcutta, India.          |
| 6 | IS: 11384-1985, “Code of Practice for Composite Construction in Structural Steel and Concrete”, Bureau of Indian Standards, New Delhi, 1985.         |

|                 |                   |          |     |   |          |   |
|-----------------|-------------------|----------|-----|---|----------|---|
| OE 943 ME       | INDUSTRIAL SAFETY |          |     |   |          |   |
| (OPEN ELECTIVE) |                   |          |     |   |          |   |
| Pre-requisites  |                   |          | L   | T | P        | C |
|                 |                   |          | 3   | - | -        | 3 |
| Evaluation      | SEE               | 60 Marks | CIE |   | 40 Marks |   |

|  |   |
|--|---|
| <b>Course Objectives :</b>   |   |
| The course is taught with the objectives of enabling the student to: |   |
| 1  | Causes for industrial accidents and preventive steps to be taken.   |
| 2  | Fundamental concepts of Maintenance Engineering.  |
| 3  | About wear and corrosion along with preventive steps to be taken  |
| 4  | The basic concepts and importance of fault tracing.   |
| 5  | The steps involved in carrying out periodic and preventive maintenance of various equipments used in industry |

|   |   |
|---|---|
| <b>Course Outcomes :</b>                                    |   |
| On completion of this course, the student will be able to : |   |
| <b>CO-1</b>   | Identify the causes for industrial accidents and suggest preventive measures.   |
| <b>CO-2</b>   | Identify the basic tools and requirements of different maintenance procedures.  |
| <b>CO-3</b>   | Apply different techniques to reduce and prevent Wear and corrosion in Industry.  |
| <b>CO-4</b>   | Identify different types of faults present in various equipments like machine tools, IC Engines, boilers etc.   |
| <b>CO-5</b>   | Apply periodic and preventive maintenance techniques as required for industrial equipments like motors, pumps and air compressors and machine tools etc |

| Course Outcome | Program Outcome |      |      |      |      |
|----------------|-----------------|------|------|------|------|
|                | PO-1            | PO-2 | PO-3 | PO-4 | PO-5 |
| <b>CO-1</b>    | 3               | -    | -    | -    | -    |
| <b>CO-2</b>    | 3               | -    | -    | -    | -    |
| <b>CO-3</b>    | 3               | -    | -    | -    | -    |
| <b>CO-4</b>    | 3               | -    | -    | -    | -    |
| <b>CO-5</b>    | -               | -    | 3    | -    | -    |

|  |
|--|
| <b>UNIT – I</b>  |
| Industrial safety: Accident, causes, types, results and control, mechanical and electrical hazards, types, causes and preventive steps/procedure, describe salient points of factories act 1948 for health and safety, wash rooms, drinking water layouts, light, cleanliness, fire, guarding, pressure vessels, etc, Safety color codes, Fire prevention and firefighting, equipment and methods. |

**UNIT – II**

Fundamentals of Maintenance Engineering: Definition and aim of maintenance engineering, Primary and secondary functions and responsibility of maintenance department, Types of maintenance, Types and applications of tools used for maintenance, Maintenance cost & its relation with replacement economy, Service life of equipment.

**UNIT – III**

Wear and Corrosion and their Prevention: Wear- types, causes, effects, wear reduction methods, lubricants-types and applications, Lubrication methods, general sketch, working and applications of Screw down grease cup, Pressure grease gun, Splash lubrication, Gravity lubrication, Wick feed lubrication, Side feed lubrication, Ring lubrication, Definition of corrosion, principle and factors affecting the corrosion, Types of corrosion, corrosion prevention methods.

**UNIT – IV**

Fault Tracing: Fault tracing-concept and importance, decision tree concept, need and applications, sequence of fault finding activities, show as decision tree, draw decision tree for problems in machine tools, hydraulic, pneumatic, automotive, thermal and electrical equipment's like, any one machine tool, Pump, Air compressor, Internal combustion engine, Boiler, Electrical motors, Types of faults in machine tools and their general causes.

**UNIT – V**

Periodic and Preventive Maintenance: Periodic inspection-concept and need, degreasing, cleaning and repairing schemes, overhauling of mechanical components, overhauling of electrical motor, common troubles and remedies of electric motor, repair complexities and its use, definition, need, steps and advantages of preventive maintenance. Steps/procedure for periodic and preventive maintenance of Machine tools, Pumps, Air compressors, Diesel generating (DG) sets, Program and schedule of preventive maintenance of mechanical and electrical equipment, advantages of preventive maintenance. Repair cycle concept and importance.

**Suggested Reading:**

|   |  |
|---|--|
| 1 | H. P. Garg, "Maintenance Engineering", S. Chand and Company                    |
| 2 | Audels, "Pump-hydraulic Compressors", Mcgraw Hill Publication                  |
| 3 | Higgins & Morrow, "Maintenance Engineering Handbook", Da Information Services. |
| 4 | Winterkorn, Hans, "Foundation Engineering Handbook", Chapman & Hall London     |



| OE 941 LA       |     | INTELLECTUAL PROPERTY RIGHTS |     |   |          |   |
|-----------------|-----|------------------------------|-----|---|----------|---|
| (OPEN ELECTIVE) |     |                              |     |   |          |   |
| Pre-requisites  |     |                              | L   | T | P        | C |
|                 |     |                              | 3   | - | -        | 3 |
| Evaluation      | SEE | 60 Marks                     | CIE |   | 40 Marks |   |

| Course Objectives :  |  |
|--|--|
| The course is taught with the objectives of enabling the student to: |  |
| 1  | Acquaint the students with basics of intellectual property rights with special reference to Indian Laws and its practices.       |
| 2  | Compare and contrast the different forms of intellectual property protection in terms of their key differences and similarities. |
| 3  | Provide an overview of the statutory, procedural, and case law underlining these processes and their interplay with litigation.  |

| Course Outcomes :   |   |
|---|---|
| On completion of this course, the student will be able to : |   |
| CO-1  | Understand the concept of intellectual property rights.                           |
| CO-2  | Develop proficiency in trademarks and acquisition of trade mark rights.           |
| CO-3  | Understand the skill of acquiring the copy rights, ownership rights and transfer. |
| CO-4  | Able to protect trade secrets, liability for misappropriations of trade secrets.  |
| CO-5  | Apply the patents and demonstration of case studies.                              |

| Course Outcome | Program Outcome |      |      |      |      |
|----------------|-----------------|------|------|------|------|
|                | PO-1            | PO-2 | PO-3 | PO-4 | PO-5 |
| CO-1           | 3               | -    | 3    | -    | -    |
| CO-2           | 3               | 3    | 3    | -    | -    |
| CO-3           | 3               | -    | 3    | -    | -    |
| CO-4           | 3               | -    | 3    | -    | -    |
| CO-5           | 3               | -    | 3    | -    | -    |

| UNIT – I  |
|---|
| Nature of Intellectual Property: Patents, Designs, Trade and Copyright. Process of Patenting and Development: technological research, innovation, patenting, development. International Scenario: International cooperation on Intellectual Property. Procedure for grants of patents, Patenting under PCT. |

| UNIT – II   |
|---|
| Trade Marks: Purpose and function of trademarks, acquisition of trade mark rights, protectable matter, selecting, and evaluating trade mark, trade mark registration processes. |

**UNIT – III**

Law of copy rights: Fundamental of copy right law, originality of material, rights of reproduction, rights to perform the work publicly, copy right ownership issues, copy right registration, notice of copy right, international copy right law. Law of patents: Foundation of patent law, patent searching process, ownership rights and transfer.

**UNIT – IV**

Trade Secrets: Trade secrete law, determination of trade secrete status, liability for misappropriations of trade secrets, protection for submission, trade secrete litigation. Unfair competition: Misappropriation right of publicity, false advertising.

**UNIT – V**

New Developments in IPR: Administration of Patent System. New developments in IPR; IPR of Biological Systems, Computer Software etc. Traditional knowledge Case Studies, IPR and IITs.

**Suggested Reading:**

|   |  |
|---|--|
| 1 | Halbert, “Resisting Intellectual Property”, Taylor & Francis Ltd, 2007.                                    |
| 2 | “Mayall, “Industrial Design”, McGraw Hill,1992   |
| 3 | “Nebel, “Product Design”, McGraw Hill,1974.  |
| 4 | “Asimov, “Introduction to Design”, Prentice Hall,1962.   |
| 5 | “Robert P. Merges, Peter S. Menell, Mark A. Lemley, “Intellectual Property in New Technological Age”,2016. |
| 6 | T. Ramappa, “Intellectual Property Rights Under WTO”, S. Chand,2008  |

| EC172          |     | MINI PROJECT |     |          |   |   |
|----------------|-----|--------------|-----|----------|---|---|
|                |     |              |     |          |   |   |
| Pre-requisites | -   |              | L   | T        | P | C |
|                |     |              | -   | -        | 4 | 2 |
| Evaluation     | SEE | -            | CIE | 50 Marks |   |   |

**Course Objectives :**

The course is taught with the objectives of enabling the student to:

|   |   |
|---|---|
| 1 | <i>To review available literature and formulate structural engineering problems</i> |
| 2 | <i>To learn the technique of writing reports and prepare presentation</i>           |

**Course Outcomes :**

On completion of this course, the student will be able to :

|      |  |
|------|--|
| CO-1 | <i>Identify problems by reviewing available literature</i>   |
| CO-2 | <i>Study different techniques used to analyze various algorithms, architectures and implementations.</i> |
| CO-3 | <i>Able to work on the solutions given problem</i>   |
| CO-4 | <i>Present solution by using his/her technique applying engineering principles.</i>                      |
| CO-5 | <i>Prepare technical report and presentation</i>   |

|     | PO1 | PO2 | PO3 | PO4 | PO5 |
|-----|-----|-----|-----|-----|-----|
| CO1 | 3   | 3   | 3   | 3   | 3   |
| CO2 | 3   | 3   | 3   | 3   | 3   |
| CO3 | 3   | 3   | 3   | 3   | 3   |
| CO4 | 3   | 3   | 3   | 3   | 3   |
| CO5 | 3   | 3   | 3   | 3   | 3   |

**Syllabus Contents:**

Mini Project will have mid semester presentation and end semester presentation. Mid semester Presentation will include identification of the problem based on the literature review on the topic referring to latest literature available.

End semester presentation should be done along with the report on identification of topic for the work and the methodology adopted involving scientific research, collection and analysis of data, determining solutions highlighting individuals' contribution. Continuous assessment of Mini Project at Mid Sem and End Sem will be monitored by the departmental committee

|                |                           |   |     |          |   |
|----------------|---------------------------|---|-----|----------|---|
| EC162          | DIGITAL SYSTEM DESIGN LAB |   |     |          |   |
|                |                           |   |     |          |   |
| Pre-requisites | -                         | L | T   | P        | C |
|                |                           | - | -   | 2        | 1 |
| Evaluation     | SEE                       | - | CIE | 50 Marks |   |

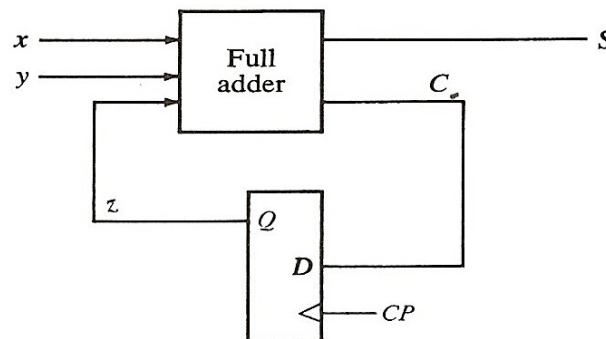
|   |   |
|---|---|
| <b>Course Outcomes:</b>                                     |   |
| On completion of this course, the student will be able to : |   |
| <b>CO-1</b>   | Write Verilog code/ System Verilog code for a given problem statement |
| <b>CO-2</b>   | Understand Simulation and Synthesis using EDA tools                   |
| <b>CO-3</b>   | Apply Verilog to VLSI Design  |

|            | <b>PO1</b> | <b>PO2</b> | <b>PO3</b> | <b>PO4</b> | <b>PO5</b> |
|------------|------------|------------|------------|------------|------------|
| <b>CO1</b> | 3          | -          | 3          | -          | 3          |
| <b>CO2</b> | 3          | -          | 3          | -          | 3          |
| <b>CO3</b> | 3          | -          | 3          | -          | 3          |

#### Verilog HDL and System Verilog

1. Implement a 4-bit pseudo-random Binary sequence generator using a linear feedback shift register with test bench?
2. Implement a 8-bit register with shift left and shift right modes of operation and test the logic with help of test bench?
3. Write Verilog program for cooking-gas delivery with following considerations (with signals request, wait, grant, fine)
  - a) Difference between two successive deliveries should be minimum of 15 days.
  - b) If user requests before 15 days, wait should be asserted
  - c) If user requests when wait is asserted, fine should be asserted
4. Generate Clock signal and write Verilog code for calculating frequency of clock signal?
5. Generate clock signal and write verilog code for skipping two clock cycles at a time and for every two clock cycles the output should be raising edge?
6. Prepare a LUT it contains the Train information such as train number, train time, number of sleeper classes and number of AC classes. Write a verilog code When user select the Train number the output should display the train information from the prepared LUT?
7. Write SV code for
  - a. Class creation
  - b. Class instance and object Creation
  - c. Accessing class properties and methods
  - d. Class Constructors
8. Write a SV code demonstrating access to static class properties and methods.

9. Write a SV code for
  - a. Parent class properties accessed using child class handle.
  - b. Parent class method is overridden in the child class.
10. Write a system Verilog code to show the usage of local keyword and protected keyword within and outside a class.
11. Write a SV codes for
  - a. Creating abstract classes
  - b. Accessing Static class member using class resolution operator
12. Define a base class with Half Adder as a Function.
  - a. Write the extended class for Full adder using the base class.
  - b. Write a module defining the functionality for 4 bit Ripple carry adder.
13. Define a parent class with a 32 bit protected parameter tmp\_addr
  1. Write a child class containing
    - i. A constructor to initialize the parameter "tmp\_addr".
    - ii. A function to increment the parameter "tmp\_addr".
  2. Write a module showcasing the usage of protected variable "tmp\_addr" after incrementing its address.
14. Define a base class with Full Adder as a Function.
  - a. Write the extended classes for D Flip-flop.
  - b. Write a module defining the functionality for the



following circuit.

15. Write constraint to create four random numbers a, b, c, d.
  1. "a" should be less than 5000 and greater than 100, and should not be divisible by 2
  2. "b" should be less than 5,000 and should be divisible by 5
  3. "c" should be in the range of 1 to 5 and include the expression  $[(a-b) : (a+b)]$
  4. "d" should be greater than all a, b and c.
16. Write a SV code for
  - a. covergroup
  - b. coverpoint
  - c. cross
  - d. bin

| EC163          |     | VLSI DESIGN LAB |     |          |   |   |
|----------------|-----|-----------------|-----|----------|---|---|
|                |     |                 |     |          |   |   |
| Pre-requisites | -   |                 | L   | T        | P | C |
|                |     |                 | -   | -        | 2 | 1 |
| Evaluation     | SEE | -               | CIE | 50 Marks |   |   |

| Course Outcomes:  |  |
|---|--|
| On completion of this course, the student will be able to : |  |
| CO-1  | Analyze various combinational and sequential logic circuits using simulation tools |
| CO-2  | Analyze various amplifiers using simulation tools                                  |
| CO-3  | Validate the functionality of digital CMOS logic circuits using EDA tools.         |

|     | PO1 | PO2 | PO3 | PO4 | PO5 |
|-----|-----|-----|-----|-----|-----|
| CO1 | 3   | -   | 3   | -   | 3   |
| CO2 | 3   | -   | 3   | -   | 3   |
| CO3 | 3   | -   | 3   | -   | 3   |

1. Design of CMOS Inverter & two input NAND Gate.
2. Design of Half Adder using NAND Gates
3. Design a Full Adder using transmission gate logic.
4. Design a Schmitt trigger circuit using CMOS logic
5. Design of 4-bit Adder using Full Adder.
6. Design a 4bit barrel shifter
7. Design of 4-bit thermometer to Binary Code converter.
8. Design and draw the layout of above Digital Circuits.
9. Analyze a two-level RC interconnect circuit for a step input
10. Analyze a tree level inductive interconnect model circuit
11. Design of Current Mirror circuit
12. Design of Current Source and Current Sink
13. Design of Op-Amp

**SEMESTER –III**

|                |                      |          |     |   |          |   |
|----------------|----------------------|----------|-----|---|----------|---|
| AC030EC        | RESEARCH METHODOLOGY |          |     |   |          |   |
| AUDIT COURSE-I |                      |          |     |   |          |   |
| Pre-requisites |                      |          | L   | T | P        | C |
|                |                      |          | 2   | - | -        | 0 |
| Evaluation     | SEE                  | 60 Marks | CIE |   | 40 Marks |   |

**Objectives:**

|  |
|--|
| 1. Learn to focus on research related activities.                      |
| 2. Learn methods to devise and develop the various research designs    |
| 3. Learn basic principles of data collection and analysis techniques   |
| 4. Learn the style and format of writing a report for technical papers |

**Outcomes:** *At the end of this course, students will be able to:*

|  |
|--|
| 1. Motivate the orientation towards research related activities                        |
| 2. Formulate the research problem, analyze research related information                |
| 3. Identify various sources for literature review and design an experimentation set-up |
| 4. Apply the basic principles of data collection and analysis techniques               |
| 5. Improve the style and format of writing a report for technical / Journal articles   |

**Program Articulation Matrix**

| Course outcome | Program outcome |     |     |     |     |     |
|----------------|-----------------|-----|-----|-----|-----|-----|
|                | PO1             | PO2 | PO3 | PO4 | PO5 | PO6 |
| CO1            |                 |     |     |     |     |     |
| CO2            |                 |     |     |     |     |     |
| CO3            |                 |     |     |     |     |     |
| CO4            |                 |     |     |     |     |     |
| CO5            |                 |     |     |     |     |     |

**UNIT – I**

**Research Methodology:** Objectives and Motivation of Research, Types of Research, Research Approaches, Significance of Research, Research Methods versus Methodology, Research and Scientific Method, Importance of Research Methodology, Research Process, Criteria of Good Research, Problems Encountered by Researchers in India, Benefits to the society in general.

**Defining the Research Problem:** Definition of Research Problem, Problem Formulation, Necessity of Defining the Problem, Technique involved in Defining a Problem.

|   |
|---|
| <b>UNIT – II</b>  |
| <b>Literature Survey:</b> Importance of Literature Survey, Sources of Information, Assessment of Quality of Journals and Articles, Information through Internet. <b>Literature Review:</b> Need of Review, Guidelines for Review, Record of Research Review.  |
| <b>UNIT – III</b>   |
| <b>Research Design:</b> Meaning of Research Design, Need of Research Design, Feature of a Good Design Important Concepts Related to Research Design, Different Research Designs, Basic Principles of Experimental Design, Developing a Research Plan, Design of Experimental Set-up, Use of Standards and Codes.  |
| <b>UNIT – IV</b>  |
| <b>Data Collection:</b> Collection of primary data, Secondary data, Data organization, Methods of data grouping, Diagrammatic representation of data, Graphic representation of data. Sample Design, Need for sampling, some important sampling definitions, Estimation of population, Role of Statistics for Data Analysis, Parametric V/s Non Parametric methods, Descriptive Statistics, Measures of central tendency and Dispersion, Hypothesis testing, Use of Statistical software. |
| <b>Data Analysis:</b> Deterministic and random data, Uncertainty analysis, Tests for significance: Chi-square, student's t-test, Regression modeling, Direct and Interaction effects, ANOVA, F- test, Time Series analysis, Autocorrelation and Autoregressive modeling.  |
| <b>UNIT – V</b>   |
| <b>Research Report Writing:</b> Format of the Research report, Synopsis, Dissertation, Thesis its Differentiation, References/Bibliography/Webliography, Technical paper writing/Journal report writing, making presentation, Use of visual aids. <b>Research Proposal Preparation:</b> Writing a Research Proposal and Research Report, Writing Research Grant Proposal.   |

## Suggested Reading:

|   |  |
|---|--|
| 1 | C.R Kothari, Research Methodology, Methods & Technique; Revised Edition, New Age International Publishers, 2004.                               |
| 2 | R. Ganesan, Research Methodology for Engineers, 1 <sup>st</sup> Edition, MJP Publishers, 2011.   |
| 3 | RatanKhananabis and SuvasisSaha, Research Methodology, 1 <sup>st</sup> Edition, Universities Press, Hyderabad, 2015.                           |
| 4 | Y.P. Agarwal, Statistical Methods: Concepts, Application and Computation, 1 <sup>st</sup> Edition, Sterling Pubs., Pvt., Ltd., New Delhi, 2004 |
| 5 | Vijay Upagade and AravindShende, Research Methodology, 1 <sup>st</sup> Edition, S. Chand & Company Ltd., New Delhi, 2009                       |
| 6 | G. Nageswara Rao, Research Methodology and Quantitative methods, 2 <sup>nd</sup> Edition, BS Publications, Hyderabad, 2012.                    |



|                 |                                    |          |     |   |          |   |
|-----------------|------------------------------------|----------|-----|---|----------|---|
| AC031           | ENGLISH FOR RESEARCH PAPER WRITING |          |     |   |          |   |
| AUDIT COURSE-II |                                    |          |     |   |          |   |
| Pre-requisites  |                                    |          | L   | T | P        | C |
|                 |                                    |          | 2   | - | -        | 0 |
| Evaluation      | SEE                                | 60 Marks | CIE |   | 40 Marks |   |

**Objectives:**

1. Understand that how to improve your writing skills and level of readability
2. Understand the nuances of language and vocabulary in writing a Research Paper.
3. Develop the content, structure, format of writing a research paper and produce original research papers without plagiarism

**Outcomes:** At the end of this course, students will be able to:

1. Interpret the nuances of research paper writing.
2. Differentiate the research paper format and citation of sources.
3. To review the research papers and articles in a scientific manner.
4. Avoid plagiarism and be able to develop their writing skills in presenting the research work.
5. Create a research paper and acquire the knowledge of how and where to publish their original research papers

**Program Articulation Matrix**

| Course outcome | Program outcome |     |     |     |     |     |
|----------------|-----------------|-----|-----|-----|-----|-----|
|                | PO1             | PO2 | PO3 | PO4 | PO5 | PO6 |
| CO1            |                 |     |     |     |     |     |
| CO2            |                 |     |     |     |     |     |
| CO3            |                 |     |     |     |     |     |
| CO4            |                 |     |     |     |     |     |
| CO5            |                 |     |     |     |     |     |

Row wise cumulative percentage weightage should be equal to 1.0.

|  |
|--|
| <b>UNIT – I</b>  |
| Features of Academic Writing<br><b>Language:</b> Clear, Correct, Concise, Inclusive; <b>Tone:</b> Formal, Objective, Cautious;<br><b>Style:</b> Appropriate, Accurate, Organized; <b>Ethics:</b> Honesty, Integrity, Responsibility, Accountability. |
| <b>UNIT – II</b>   |
| <b>Kinds of Academic Writing</b><br>Essays, Reports, Reviews, Abstracts, Proposals   |
| <b>UNIT – III</b>  |
| <b>Academic Writing Skills</b><br>Paraphrasing, Summarizing, Quoting, Rewriting, Expansion   |
| <b>UNIT – IV</b>   |

**Research Process**

Selection of Topic, Formulation of Hypothesis, Collection of Data, Analysis of Data, Interpretation of Data, Presentation of Data

**UNIT – V****Structure of a Research Document**

Title, Abstract, Introduction, Literature Survey, Methodology, Discussion, Findings/Results, Conclusion, Documenting Sources (IEEE style)

**References:**

|   |   |
|---|---|
| 1 | Bailey, S. (2014). <i>Academic writing: A handbook for international students</i> . Routledge.  |
| 2 | Gillett, A., Hammond, A., & Martala, M. (2009). <i>Inside track: Successful academic writing</i> . Essex: Pearson Education Limited.  |
| 3 | Griffin, G. (2006). <i>Research methods for English studies</i> . Edinburgh: Edinburgh University Press.  |
| 4 | Silyn-Roberts, Heather. (2013). <i>Writing for Science and Engineering: Papers, Presentations and Reports</i> (2nd ed.). Elsevier.  |
| 5 | Lipson, Charles (2011). <i>Cite right: A quick guide to citation styles; MLA, APA, Chicago, the sciences, professions, and more</i> (2nd ed.). Chicago [u.a.]: University of Chicago Press. |

|                 |                                    |          |     |   |          |   |
|-----------------|------------------------------------|----------|-----|---|----------|---|
| AC032           | DISASTER MITIGATION AND MANAGEMENT |          |     |   |          |   |
| AUDIT COURSE-II |                                    |          |     |   |          |   |
| Pre-requisites  |                                    |          | L   | T | P        | C |
|                 |                                    |          | 2   | - | -        | 0 |
| Evaluation      | SEE                                | 60 Marks | CIE |   | 40 Marks |   |

**Objectives:**

1. To impart knowledge in students about the nature, causes, consequences and mitigation measures of the various natural disasters
2. To enable the students to understand risks, vulnerabilities and human errors associated with human induced disasters
3. To enable the students to understand and assimilate the impacts of any disaster on the affected area depending on its position/ location, environmental conditions, demographic, etc.

**Outcomes:** At the end of this course, students will be able to:

1. Learn to demonstrate a critical understanding of key concepts in disaster risk reduction
2. Humanitarian response
3. Critically evaluate disaster risk reduction and humanitarian response policy and Practice from multiple perspectives.
4. Develop an understanding of standards of humanitarian response and practical relevance in specific types of disasters and conflict situations.
5. Critically understand the strengths and weaknesses of disaster management approaches, planning and programming in different countries, particularly their home country or the countries they work in.

**Program Articulation Matrix**

| Course outcome | Program outcome |     |     |     |     |     |
|----------------|-----------------|-----|-----|-----|-----|-----|
|                | PO1             | PO2 | PO3 | PO4 | PO5 | PO6 |
| CO1            |                 |     |     |     |     |     |
| CO2            |                 |     |     |     |     |     |
| CO3            |                 |     |     |     |     |     |
| CO4            |                 |     |     |     |     |     |
| CO5            |                 |     |     |     |     |     |

Row wise cumulative percentage weightage should be equal to 1.0.

**UNIT – I**

*Introduction:* Disaster: Definition, Factors and Significance; Difference between Hazard and Disaster; Natural and Manmade Disasters: Difference, Nature, Types and Magnitude.

**UNIT – II**

*Repercussions of Disasters and Hazards:* Economic Damage, Loss of Human and Animal Life, Destruction of Ecosystem.

*Natural Disasters:* Earthquakes, Volcanisms, Cyclones, Tsunamis, Floods, Droughts and Famines, Landslides and Avalanches, Man-made disaster: Nuclear Reactor Meltdown, Industrial Accidents, Oil Slicks and Spills, Outbreaks of Disease and Epidemics, War and Conflicts.

**UNIT – III**

|  |
|--|
| <i>Disasters Prone Areas in India:</i> Study of Seismic Zones; Areas Prone to Floods and Droughts, Landslides and Avalanches; Areas Prone to Cyclonic and Coastal Hazards with Special Reference to Tsunami; Post-Disaster Diseases and Epidemics  |
| <b>UNIT – IV</b>   |
| <i>Disaster Preparedness:</i> Monitoring of Phenomena Triggering a Disaster or Hazard; Evaluation of Risk: Application of Remote Sensing, Data from Meteorological and Other Agencies, Media Reports: Governmental and Community Preparedness.   |
| <b>UNIT – V</b>  |
| <i>Disaster Risk:</i> Concept and Elements, Disaster Risk Reduction, Global and National Disaster Risk Situation. Techniques of Risk Assessment, Global Co-Operation in Risk Assessment and Warning, People's Participation in Risk Assessment. Strategies for Survival. Meaning, Concept and Strategies of Disaster Mitigation, Emerging Trends in Mitigation. Structural Mitigation and Non-Structural Mitigation, Programs of Disaster Mitigation in India. |

**References:**

|   |  |
|---|--|
| 1 | R. Nishith, Singh AK, “ <i>Disaster Management in India: Perspectives, issues and strategies</i> ”, New Royal Book Company.        |
| 2 | Sahni, Pardeep (Eds.), “ <i>Disaster Mitigation Experiences and Reflections</i> ”, PHI, New Delhi.                                 |
| 3 | Goel S. L., “ <i>Disaster Administration and Management Text and Case Studies</i> ”, Deep & Deep Publication Pvt. Ltd., New Delhi. |

|                 |                                  |          |     |   |          |   |
|-----------------|----------------------------------|----------|-----|---|----------|---|
| AC033           | SANSKRIT FOR TECHNICAL KNOWLEDGE |          |     |   |          |   |
| AUDIT COURSE-II |                                  |          |     |   |          |   |
| Pre-requisites  |                                  |          | L   | T | P        | C |
|                 |                                  |          | 2   | - | -        | 0 |
| Evaluation      | SEE                              | 60 Marks | CIE |   | 40 Marks |   |

**Objectives:**

1. To get a working knowledge in illustrious Sanskrit, the scientific language in the world
2. To make the novice Learn the Sanskrit to develop the logic in mathematics, science & other subjects
3. To explore the huge knowledge from ancient Indian literature

**Outcomes:**At the end of this course, students will be able to:

1. Develop passion towards Sanskrit language
2. Decipher the latent engineering principles from Sanskrit literature
3. Correlates the technological concepts with the ancient Sanskrit history.
4. Develop knowledge for the technological progress
5. Explore the avenue for research in engineering with aid of Sanskrit

**Program Articulation Matrix**

| Course outcome | Program outcome |     |     |     |     |     |
|----------------|-----------------|-----|-----|-----|-----|-----|
|                | PO1             | PO2 | PO3 | PO4 | PO5 | PO6 |
| CO1            |                 |     |     |     |     |     |
| CO2            |                 |     |     |     |     |     |
| CO3            |                 |     |     |     |     |     |
| CO4            |                 |     |     |     |     |     |
| CO5            |                 |     |     |     |     |     |

Row wise cumulative percentage weightage should be equal to 1.0.

|  |
|--|
| <b>UNIT – I</b>  |
| <i>Introduction to Sanskrit Language:</i> Sanskrit Alphabets-vowels-consonants- significance of Amarakosa-parts of Speech-Morphology-creation of new words-significance of synonyms-sandhi-samasa-sutras-active and passive Voice-Past/Present/Future Tense-Syntax-Simple Sentences (elementary treatment only)  |
| <b>UNIT – II</b>   |
| <i>Role of Sanskrit in Basic Sciences:</i> Brahmagupthas lemmas (second degree indeterminate equations), sum of squares of n-terms of AP- sulba, sutram or baudhayana theorem (origination of Pythagoras theorem)-value of pie-Madhava's sine and cosine theory (origination of Taylor's series). The measurement system-time-mass-length-temp, Matter elasticity-optics-speed of light (origination of Michaelson and Morley theory). |
| <b>UNIT – III</b>  |
| <i>Role of Sanskrit in Engineering-I (Civil, Mechanical, Electrical and Electronics Engineering):</i><br>Building construction-soil testing-mortar-town planning-Machine definition-crucible-furnace-air blower- Generation of electricity in a cell-magnetism-Solar system-Sun: The source of energy, the earth-Pingalachandasutram (origination of digital logic system)   |
| <b>UNIT – IV</b>   |

*Role of Sanskrit in Engineering-II (Computer Science Engineering & Information Technology):* Computer languages and the Sanskrit languages-computer command words and the vedic command words-analogy of pramana in memamsa with operators in computer language-sanskrit analogy of physical sequence and logical sequence, programming.

**UNIT – V**

*Role of Sanskrit in Engineering-III (Bio-technology and Chemical Engineering):* Classification of plants- plants, the living-plants have senses-classification of living creatures, Chemical laboratory location and layout- equipment-distillation vessel-kosthiyanthram

**References:**

|   |   |
|---|---|
| 1 | M Krishnamachariar, “ <i>History of Classical Sanskrit Literature</i> ”, TTD Press, 1937.   |
| 2 | M.R. Kale, “ <i>A Higher Sanskrit Grammar: For the Use of School and College Students</i> ”, MotilalBanarsidass Publishers, 2015. |
| 3 | Kapail Kapoor, “ <i>Language, Linguistics and Literature: The Indian Perspective</i> ”, ISBN- 10: 8171880649, 1994.               |
| 4 | “ <i>Pride of India</i> ”, SamskritaBharati Publisher, ISBN: 81-87276 27-4, 2007.   |
| 5 | Shri Rama Verma, “ <i>Vedas the source of ultimate science</i> ”, Nag publishers, 2005.   |

|                 |                 |          |     |   |          |   |
|-----------------|-----------------|----------|-----|---|----------|---|
| AC034           | VALUE EDUCATION |          |     |   |          |   |
| AUDIT COURSE-II |                 |          |     |   |          |   |
| Pre-requisites  |                 |          | L   | T | P        | C |
|                 |                 |          | 2   | - | -        | 0 |
| Evaluation      | SEE             | 60 Marks | CIE |   | 40 Marks |   |

**Objectives:**

1. Understand the need and importance of Values for self-development and for National development.
2. Imbibe good human values and Morals
3. Cultivate individual and National character.

**Outcomes:**At the end of this course, students will be able to:

1. Gain necessary Knowledge for self-development
2. Learn the importance of Human values and their application in day to day professional life.
3. Appreciate the need and importance of interpersonal skills for successful career and social life
4. Emphasize the role of personal and social responsibility of an individual for all-round growth.
5. Develop a perspective based on spiritual outlook and respect women, other religious practices, equality, non-violence and universal brotherhood.

**Program Articulation Matrix**

|            | <b>PO1</b> | <b>PO2</b> | <b>PO3</b> | <b>PO4</b> | <b>PO5</b> |
|------------|------------|------------|------------|------------|------------|
| <b>CO1</b> |            |            |            |            |            |
| <b>CO2</b> |            |            |            |            |            |
| <b>CO3</b> |            |            |            |            |            |
| <b>CO4</b> |            |            |            |            |            |
| <b>CO5</b> |            |            |            |            |            |

**UNIT – I**

*Human Values, Ethics and Morals:* Concept of Values, Indian concept of humanism, human values; Values for self-development, Social values, individual attitudes; Work ethics, moral and non- moral behaviour, standards and principles based on religion, culture and tradition.

**UNIT – II**

*Value Cultivation, and Self-management:* Need and Importance of cultivation of values such as Sense-of Duty, Devotion to work, Self-reliance, Confidence, Concentration, Integrity & discipline, and Truthfulness.

**UNIT – III**

*Spiritual outlook and social values:* Personality and Behavior, Scientific attitude and Spiritual (soul) outlook; Cultivation of Social Values Such as Positive Thinking, Punctuality, Love & Kindness, avoiding fault finding in others, Reduction of anger, forgiveness, Dignity of labour, True friendship, Universal brotherhood and religious tolerance.

**UNIT – IV**

*Values in Holy Books:*Self-management and Good health; internal & external cleanliness, Holy books versus Blind faith, Character and Competence, Equality, Nonviolence, Humility, Role of Women.

**UNIT – V**

*Dharma, Karma and Guna:*Concept of soul; Science of Reincarnation, Character and Conduct, Concept of Dharma; Cause and Effect based Karma Theory; The qualities of Devine and Devilish; Satwic, Rajasic and Tamasicgunas.

**References:**

|   |   |
|---|---|
| 1 | Chakroborty, S.K., “ <i>Values &amp; Ethics for organizations Theory and practice</i> ”, Oxford University Press, New Delhi, 1998.        |
| 2 | 2. Jaya DayalGoyandaka, “ <i>Srimad Bhagavad Gita with Sanskrit Text</i> ”, Word Meaning and Prose Meaningl, Gita Press, Gorakhpur, 2017. |



|                 |                           |          |     |   |          |   |
|-----------------|---------------------------|----------|-----|---|----------|---|
| AC035           | STRESS MANAGEMENT BY YOGA |          |     |   |          |   |
| AUDIT COURSE-II |                           |          |     |   |          |   |
| Pre-requisites  |                           |          | L   | T | P        | C |
|                 |                           |          | 2   | - | -        | 0 |
| Evaluation      | SEE                       | 60 Marks | CIE |   | 40 Marks |   |

**Objectives:**

1. Creating awareness about different types of stress and the role of yoga in the management of stress.
2. Promotion of positive health and overall wellbeing (Physical, mental, emotional, social and spiritual).
3. Prevention of stress related health problems by yoga practice.

**Outcomes:** At the end of this course, students will be able to:

1. Understand yoga and its benefits.
2. Enhance Physical strength and flexibility.
3. Learn to relax and focus.
4. Relieve physical and mental tension through asanas.
5. Improve work performance and efficiency.

**Program Articulation Matrix**

| Course outcome | Program outcome |     |     |     |     |     |
|----------------|-----------------|-----|-----|-----|-----|-----|
|                | PO1             | PO2 | PO3 | PO4 | PO5 | PO6 |
| CO1            |                 |     |     |     |     |     |
| CO2            |                 |     |     |     |     |     |
| CO3            |                 |     |     |     |     |     |
| CO4            |                 |     |     |     |     |     |
| CO5            |                 |     |     |     |     |     |

Row wise cumulative percentage weightage should be equal to 1.0.

**UNIT – I**

*Meaning and Definition of Yoga* - Historical perspective of Yoga - Principles of Astanga Yoga by Patanjali.

**UNIT – II**

*Meaning and Definition of Stress*- Types of stress - Eustress and Distress. Anticipatory Anxiety and Intense Anxiety and depression. Meaning of Management- Stress Management.

**UNIT – III**

*Concept of Stress According to Yoga*- Stress assessment methods - Role of Asana, Pranayama and Meditation in the management of stress

**UNIT – IV**

*Asanas*- (5 Asanas in each posture) - Warm up - Standing Asanas - Sitting Asanas - Prone Asanas - Supine asanas - Surya Namaskar.

**UNIT – V**

*Pranayama*- Anulom and Vilom Pranayama - Nadishudhi Pranayama - Kapalabhati Pranayama - Bhramari Pranayama - Nadanusandhana Pranayama.

*Meditation Techniques*: Om Meditation - Cyclic meditation: Instant Relaxation technique (QRT), Quick Relaxation Technique (QRT), Deep Relaxation Technique (DRT)

**References:**

|   |  |
|---|--|
| 1 | Janardhan Swami Yogabhyasi Mandal, “ <i>Yogic Asanas for Group Training - Part-I</i> ”, , Nagpur.                              |
| 2 | AdvaitaAshrama, “ <i>Swami Vivekananda, “Rajayoga or Conquering the Internal Nature”</i> ”, (Publication Department), Kolkata. |
| 3 | Nagendra H.R and Nagaratna R, “ <i>Yoga Perspective in Stress Management</i> ”, Swami Vivekananda Yoga Prakashan, Bangalore.   |

|                 |   |          |     |   |          |   |
|-----------------|---|----------|-----|---|----------|---|
| AC036           | PERSONALITY DEVELOPMENT THROUGH LIFE ENLIGHTENMENT SKILLS |          |     |   |          |   |
| AUDIT COURSE-II |   |          |     |   |          |   |
| Pre-requisites  |   |          | L   | T | P        | C |
|                 |   |          | 2   | - | -        | 0 |
| Evaluation      | SEE   | 60 Marks | CIE |   | 40 Marks |   |

**Objectives:**

|  |
|--|
| 1. To learn to achieve the highest goal happily                                |
| 2. To become a person with stable mind, pleasing personality and determination |
| 3. To awaken wisdom in students  |

**Outcomes:**At the end of this course, students will be able to:

|  |
|--|
| 1. Develop their personality and achieve their highest goal of life. |
| 2. Lead the nation and mankind to peace and prosperity.              |
| 3. Practice emotional self-regulation.                               |
| 4. Develop a positive approach to work and duties.                   |
| 5. Develop a versatile personality.                                  |

**Program Articulation Matrix**

| Course outcome | Program outcome |     |     |     |     |     |
|----------------|-----------------|-----|-----|-----|-----|-----|
|                | PO1             | PO2 | PO3 | PO4 | PO5 | PO6 |
| CO1            |                 |     |     |     |     |     |
| CO2            |                 |     |     |     |     |     |
| CO3            |                 |     |     |     |     |     |
| CO4            |                 |     |     |     |     |     |
| CO5            |                 |     |     |     |     |     |

Row wise cumulative percentage weightage should be equal to 1.0.

|  |
|--|
| <b>UNIT – I</b>  |
| <i>Neetisatakam – Holistic Development of Personality-</i> Verses 19, 20, 21, 22 (Wisdom) - Verses 29, 31, 32 (Pride and Heroism) - Verses 26,28,63,65 (Virtue)  |
| <b>UNIT – II</b>   |
| <i>Neetisatakam – Holistic Development of Personality (cont'd)</i> - Verses 52, 53, 59 (don'ts) - Verses 71,73,75 & 78 (do's) - Approach to day to day works and duties.   |
| <b>UNIT – III</b>  |
| <i>Introduction to Bhagavadgeetha for Personality Development - Shrimad Bhagavadgeetha:</i> Chapter 2 – Verses 41, 47, 48 - Chapter 3 – Verses 13,21,27,35 - Chapter 6 – Verses 5,13,17,23,35 - Chapter 18 –Verses 45, 46, 48 Chapter – 6: Verses 5, 13, 17, 23, 35; Chapter – 18: Verses 45, 46, 48 |
| <b>UNIT – IV</b>   |
| <i>Statements of Basic Knowledge - Shrimad Bhagavadgeetha:</i> Chapter 2- Verses 56, 62,68 - Chapter 12 – Verses 13, 14, 15, 16, 17, 18 - Personality of Role model from Shrimad BhagawatGeeta.  |
| <b>UNIT – V</b>  |
| <i>Role of Bhagavadgeetha in the Present Scenario</i> - Chapter 2 – Verses 17 - Chapter 3 – Verses 36, 37, 42 - Chapter 4 – Verses 18, 38, 39 - Chapter 18 – Verses 37, 38, 63.  |

[PTO]

**References:**

|   |  |
|---|--|
| 1 | Swami SwarupanandaAdvaita Ashram “ <i>Srimad Bhagavad Gita</i> ”, (Publication Department), Kolkata                  |
| 2 | P.Gopinath, “ <i>Bhartrihari’s Three Satakam (Niti-sringar-vairagya)</i> ”, Rashtriya Sanskrit Sansthanam, New Delhi |

|                 |                       |          |     |   |          |   |
|-----------------|-----------------------|----------|-----|---|----------|---|
| AC037           | CONSTITUTION OF INDIA |          |     |   |          |   |
| AUDIT COURSE-II |                       |          |     |   |          |   |
| Pre-requisites  |                       |          | L   | T | P        | C |
|                 |                       |          | 2   | - | -        | 0 |
| Evaluation      | SEE                   | 60 Marks | CIE |   | 40 Marks |   |

**Objectives:**

1. Understand the premises informing the twin themes of liberty and freedom from a civil rights perspective
2. To address the growth of Indian opinion regarding modern Indian intellectuals' constitutional role
3. Entitlement to civil and economic rights as well as the emergence of nationhood in the early years of Indian nationalism.

**Outcomes:** At the end of this course, students will be able to:

1. Discuss the growth of the demand for civil rights in India for the bulk of Indians before the arrival of Gandhi in Indian politics.
2. Discuss the intellectual origins of the framework of argument that informed the conceptualization of social reforms leading to revolution in India.
3. Discuss the circumstances surrounding the foundation of the Congress Socialist Party [CSP] under the leadership of Jawaharlal Nehru
4. The eventual failure of the proposal of direct elections through adult suffrage in the Indian Constitution.
5. Discuss the passage of the Hindu Code Bill of 1956.

**Program Articulation Matrix**

| Course outcome | Program outcome |     |     |     |     |     |
|----------------|-----------------|-----|-----|-----|-----|-----|
|                | PO1             | PO2 | PO3 | PO4 | PO5 | PO6 |
| CO1            |                 |     |     |     |     |     |
| CO2            |                 |     |     |     |     |     |
| CO3            |                 |     |     |     |     |     |
| CO4            |                 |     |     |     |     |     |
| CO5            |                 |     |     |     |     |     |

Row wise cumulative percentage weightage should be equal to 1.0.

**UNIT – I**

*History of Making of the Indian Constitution:* History, Drafting Committee, (Composition & Working) Philosophy of the Indian Constitution: Preamble, Salient Features.

**UNIT – II**

*Contours of Constitutional Rights & Duties:* Fundamental Rights, Right to Equality, Right to Freedom, Right against Exploitation, Right to Freedom of Religion, Cultural and Educational Rights, Right to Constitutional Remedies, Directive Principles of State Policy, Fundamental Duties.

**UNIT – III**

*Organs of Governance:* Parliament, Composition, Qualifications and Disqualifications, Powers and Functions, Executive, President, Governor, Council of Ministers, Judiciary, Appointment and Transfer of Judges, Qualifications. Powers and Functions.

**UNIT – IV**

*Local Administration:* District's Administration head: Role and Importance, Municipalities: Introduction, Mayor and role of Elected Representative CEO of Municipal Corporation, Panchayat raj: Introduction, PRI: Zilla Panchayat, Elected officials and their roles, CEO Zilla Panchayat: Position and role. Block level: Organizational Hierarchy (Different departments), Village level: Role of Elected and Appointed officials, Importance of grass root democracy.

**UNIT – V**

*Election Commission:* Election Commission: Role and Functioning. Chief Election Commissioner and Election Commissioners. State Election Commission: Role and Functioning. Institute and Bodies for the welfare of SC/ST/OBC and women.

**References:**

|   |   |
|---|---|
| 1 | <i>"The Constitution of India"</i> , 1950 (Bare Act), Government Publication.                   |
| 2 | Dr. S. N. Busi, <i>"Dr. B. R. Ambedkar framing of Indian Constitution"</i> , 1st Edition, 2015. |
| 3 | M. P. Jain, <i>"Indian Constitution Law"</i> , 7th Edn., Lexis Nexis, 2014.                     |
| 4 | D.D. Basu, <i>"Introduction to the Constitution of India"</i> , Lexis Nexis, 2015.              |

|                 |                  |          |     |   |          |   |
|-----------------|------------------|----------|-----|---|----------|---|
| AC038           | PEDAGOGY STUDIES |          |     |   |          |   |
| AUDIT COURSE-II |                  |          |     |   |          |   |
| Pre-requisites  |                  |          | L   | T | P        | C |
|                 |                  |          | 2   | - | -        | 0 |
| Evaluation      | SEE              | 60 Marks | CIE |   | 40 Marks |   |

**Objectives:**

|  |
|--|
| 1. To present the basic concepts of design and policies of pedagogy studies.   |
| 2. To provide understanding of the abilities and dispositions with regard to teaching techniques, curriculum design and assessment practices and familiarize various theories of learning and their connection to teaching practice. |
| 3. To create awareness about the practices followed by DFID, other agencies and other researchers and provide understanding of critical evidence gaps that guides the professional development                                       |

**Outcomes:**At the end of this course, students will be able to:

|  |
|--|
| 1. Illustrate the pedagogical practices followed by teachers in developing countries both in formal and informal classrooms. |
| 2. Examine the effectiveness of pedagogical practices.   |
| 3. Understand the concept, characteristics and types of educational research and perspectives of research.                   |
| 4. Describe the role of classroom practices, curriculum and barriers to learning.  |
| 5. Understand Research gaps and learn the future directions.   |

**Program Articulation Matrix**

| Course outcome | Program outcome |     |     |     |     |     |
|----------------|-----------------|-----|-----|-----|-----|-----|
|                | PO1             | PO2 | PO3 | PO4 | PO5 | PO6 |
| CO1            |                 |     |     |     |     |     |
| CO2            |                 |     |     |     |     |     |
| CO3            |                 |     |     |     |     |     |
| CO4            |                 |     |     |     |     |     |
| CO5            |                 |     |     |     |     |     |

Row wise cumulative percentage weightage should be equal to 1.0.

**UNIT – I**

*Introduction and Methodology:* Aims and rationale, Policy background, Conceptual framework and terminology - Theories of learning, Curriculum, Teacher education - Conceptual framework, Research questions, Overview of methodology and Searching.

**UNIT – II**

*Thematic Overview:* Pedagogical practices followed by teachers in formal and informal classrooms in developing countries - Curriculum, Teacher education

**UNIT – III**

*Evidence on the Effectiveness of Pedagogical Practices:* Methodology for the in depth stage: quality assessment of included studies - How can teacher education (curriculum and Practicum) and the school curriculum and guidance material best support effective pedagogy? - Theory of change - Strength and nature of the body of evidence for effective pedagogical practices - Pedagogic theory and pedagogical approaches – Teachers attitudes and beliefs and pedagogic strategies.

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| <b>UNIT – IV</b>   |
| <i>Professional Development:</i> Alignment with classroom practices and follow up support - Support from the head teacher and the community – Curriculum and assessment - Barriers to learning: Limited resources and large class sizes. |
| <b>UNIT – V</b>  |
| <i>Research Gaps and Future Directions:</i> Research design – Contexts – Pedagogy - Teacher education - Curriculum and assessment – Dissemination and research impact.   |

**References:**

|   |  |
|---|--|
| 1 | Ackers J, Hardman F, “ <i>Classroom Interaction in Kenyan Primary Schools, Compare</i> ”, 31 (2): 245 – 261, 2001.   |
| 2 | 2. Agarwal M, “ <i>Curricular Reform in Schools: The importance of evaluation</i> ”, Journal of Curriculum Studies, 36 (3): 361 – 379, 2004.   |
| 3 | Akyeampong K, “ <i>Teacher Training in Ghana – does it count? Multisite teacher education research project (MUSTER)</i> ”, Country Report 1. London: DFID, 2003.   |
| 4 | Akyeampong K, Lussier K, Pryor J, Westbrook J, “ <i>Improving teaching and learning of Basic Maths and Reading in Africa: Does teacher Preparation count?</i> ” International Journal Educational Development, 33 (3): 272- 282, 2013. |
| 5 | Alexander R J, “ <i>Culture and Pedagogy: International Comparisons in Primary Education</i> ”, Oxford and Boston: Blackwell, 2001.  |
| 6 | Chavan M, Read India: “ <i>A mass scale, rapid, learning to read campaign</i> ”, 2003  |



|                 |                    |          |     |   |          |   |
|-----------------|--------------------|----------|-----|---|----------|---|
| AC039           | E-WASTE MANAGEMENT |          |     |   |          |   |
| AUDIT COURSE-II |                    |          |     |   |          |   |
| Pre-requisites  |                    |          | L   | T | P        | C |
|                 |                    |          | 2   | - | -        | 0 |
| Evaluation      | SEE                | 60 Marks | CIE |   | 40 Marks |   |

**Objectives:**

|  |
|--|
| 1. Introduction to E-Waste management                        |
| 2. Understanding on resource efficiency and circular economy |
| 3. E-waste Management rules 2016                             |
| 4. RoHS compliances/directives to EEE                        |

**Outcomes:** *At the end of this course, students will be able to:*

|  |
|--|
| 1. Complete understanding on E-Waste management  |
| 2. Understanding on effective recycling methodologies for e-waste management                       |
| 3. Overall understanding about E-waste Management rules 2016 and strategies for e-waste management |
| 4. Understanding on RoHS compliances for EEE products  |

**Program Articulation Matrix**

| <b>Course outcome</b> | <b>Program outcome</b> |            |            |            |            |            |
|-----------------------|------------------------|------------|------------|------------|------------|------------|
|                       | <b>PO1</b>             | <b>PO2</b> | <b>PO3</b> | <b>PO4</b> | <b>PO5</b> | <b>PO6</b> |
| <b>CO1</b>            |                        |            |            |            |            |            |
| <b>CO2</b>            |                        |            |            |            |            |            |
| <b>CO3</b>            |                        |            |            |            |            |            |
| <b>CO4</b>            |                        |            |            |            |            |            |
| <b>CO5</b>            |                        |            |            |            |            |            |

Row wise cumulative percentage weightage should be equal to 1.0.

**UNIT – I**

Waste Electrical and Electronic Equipment (WEEE): Flows, Quantities and Management, a Global Scenario; The Importance of Waste Management; Types of Waste- Solid and Liquid; Criteria for EEE/E-Waste Classification; Multivariate Model for E-Waste Estimation; Environmental and Health Effects of Waste Management, Inventorisation of E-Waste and Emerging trends in E-waste disposal with bench marks for depollution - global scenario; Dumping, Burning and Landfill: Impact on the Environment

**UNIT – II**

Effective Waste Management and Disposal Strategies; Legislative Influence on Electronics Recycling; Waste Management Rules and Their Amendments; Extended Producer Responsibility (EPR) in E-Waste Management; The Role of Collective versus Individual Producer Responsibility in E-Waste Management

**UNIT – III**

Electronic Waste: Public Health Implications; Restriction of Hazardous Substances (RoHS) Directives in Electrical and Electronic Equipment; Materials Used in Manufacturing Electrical and Electronic Products

**UNIT – IV**

Recycling and Resource Management: Ecological and Economical Valuation; Life Cycle Assessment (LCA) Approach to Waste Management System; Environmental Incentives for Recycling and Life Cycle Analysis of Materials Recycling Electronic Waste: Challenges and Opportunities for Sustainable Management; Resource Recovery from E-waste: Efficiency and

|   |
|---|
| Circular Economy; Integrated Approach to E-Waste Recycling: Recycling and Recovery Technologies, Recycling and Recovery Technologies. |
| <b>UNIT – V</b>   |
| Cases studies: E-waste Generation, collection and recycling   |

**References:**

|   |  |
|---|--|
| 1 | Electronic Waste Management and Treatment Technology, Editors: MajetiNarasimhaVara Prasad MeththikaVithanage                                   |
| 2 | Electronic Waste Management, Edited by R. E. Hester, R. M. Harrison, RSC Publishing 2009   |
| 3 | Solid Waste Technology & Management, Christensen, T., Ed., Wiley and Sons., 2011   |
| 4 | Electronics Waste Management: An India Perspective. Front Cover. Sandip Chatterjee. Lap Lambert Academic Publishing GmbH KG, 2010 - Electronic |
| 5 | Handbook of Electronic Waste Management, International Best Practices and Case studies, Elsevier, 2019   |
| 6 | E-waste: Implications, regulations, and management in India and current global best practices. Author(s): Rakesh Johri, TERI Press             |

|                      |                |   |     |           |    |    |
|----------------------|----------------|---|-----|-----------|----|----|
| EC181                | DISSERTATION-I |   |     |           |    |    |
| DISSERTATION PHASE-I |                |   |     |           |    |    |
| Pre-requisites       | -              |   | L   | T         | P  | C  |
|                      |                |   | -   | -         | 20 | 10 |
| Evaluation           | SEE            | - | CIE | 100 Marks |    |    |

|                            |   |
|----------------------------|---|
| <b>Course Objectives :</b> |   |
| 1                          | <i>Identification of the research problem</i> |
| 2                          | <i>Discussion of literature survey.</i>       |

|                          |  |
|--------------------------|--|
| <b>Course Outcomes :</b> |  |
| <b>CO-1</b>              | <i>Identification of the objectives of the Research Problem.</i>   |
| <b>CO-2</b>              | <i>Ability to update the latest literature in chosen area of research &amp; establishment of the scope of work.</i>      |
| <b>CO-3</b>              | <i>Development of the methodology for the chosen research problem and perform basic theoretical /experiment studies.</i> |

|             | <b>PO-1</b> | <b>PO-2</b> | <b>PO-3</b> | <b>PO-4</b> | <b>PO-5</b> |
|-------------|-------------|-------------|-------------|-------------|-------------|
| <b>CO-1</b> | 3           | 3           | 3           | 3           | 3           |
| <b>CO-2</b> | 3           | 3           | 3           | 3           | 3           |
| <b>CO-3</b> | 3           | 3           | 3           | 3           | 3           |

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| <b>Contents:</b>   |
| Each student will be attached to a faculty member/guide for project. The student will carry out the project which may be development of Software / Hardware / Simulation studies / Design analysis / Experimental related to his/her specialization. The work will be monitored regularly by the guide.  |
| At the end of the semester student will write the report on the work done and submit to the guide. Student has to present his/her work before two faculty members (one guide and other to be appointed by chairman BOS) on a fixed day during last week of the semester in which project is offered. The sessional marks will be awarded jointly by these examiners based on the report, presentation and viva voice |

**SEMESTER – IV**

|                       |                 |     |     |           |    |    |
|-----------------------|-----------------|-----|-----|-----------|----|----|
| EC182                 | DISSERTATION-II |     |     |           |    |    |
| DISSERTATION PHASE-II |                 |     |     |           |    |    |
| Pre-requisites        | -               |     | L   | T         | P  | C  |
|                       |                 |     | -   | -         | 32 | 16 |
| Evaluation            | SEE             | 100 | CIE | 100 Marks |    |    |

**Course Objectives :**

|   |   |
|---|---|
| 1 | <i>Identification of the research problem</i> |
| 2 | <i>Discussion of literature survey.</i>       |

**Course Outcomes :**

|             |  |
|-------------|--|
| <b>CO-1</b> | <i>Expand the defined Research Problem for the dissertation work.</i>  |
| <b>CO-2</b> | <i>Conduct of Laboratory/analytical/ software studies</i>  |
| <b>CO-3</b> | <i>Analysis of Data, development of models, offer solutions to the research problem and provide conclusions of the work.</i> |

|             | <b>PO-1</b> | <b>PO-2</b> | <b>PO-3</b> | <b>PO-4</b> | <b>PO-5</b> |
|-------------|-------------|-------------|-------------|-------------|-------------|
| <b>CO-1</b> | 3           | 3           | 3           | 3           | 3           |
| <b>CO-2</b> | 3           | 3           | 3           | 3           | 3           |
| <b>CO-3</b> | 3           | 3           | 3           | 3           | 3           |

**Contents:**

The student will carry out the project under allotted supervisor, which may be development of Software / Hardware / Simulation studies / Design analysis / Experimental related to his/her specialization. The work will be monitored regularly by the guide. At the end of the semester student will write the report on the work done and submit to the guide. Student has to present his/her work before two faculty members (one guide and other to be appointed by chairman BOS) on a fixed day during last week of the semester in which project is offered. The final marks will be allotted based on the report, presentation and viva voce conducted by the external examiner whose name is suggested by Chairman BOS